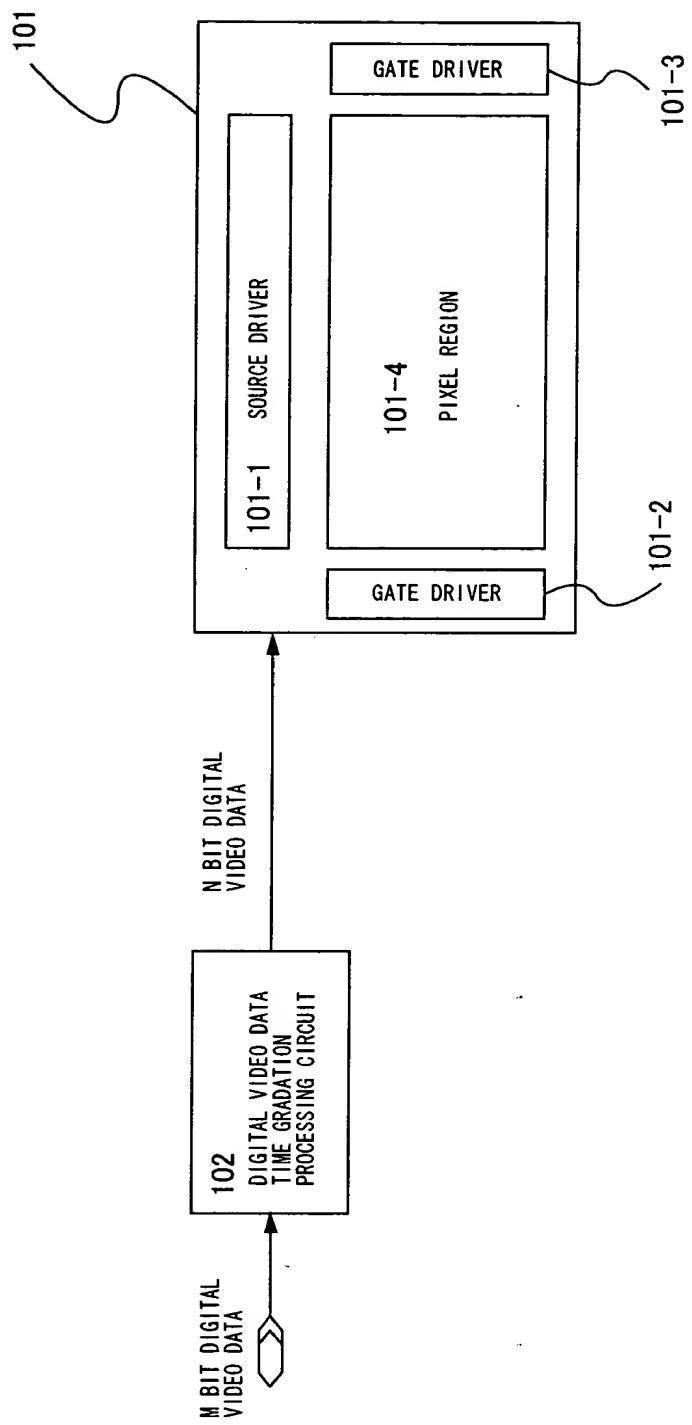
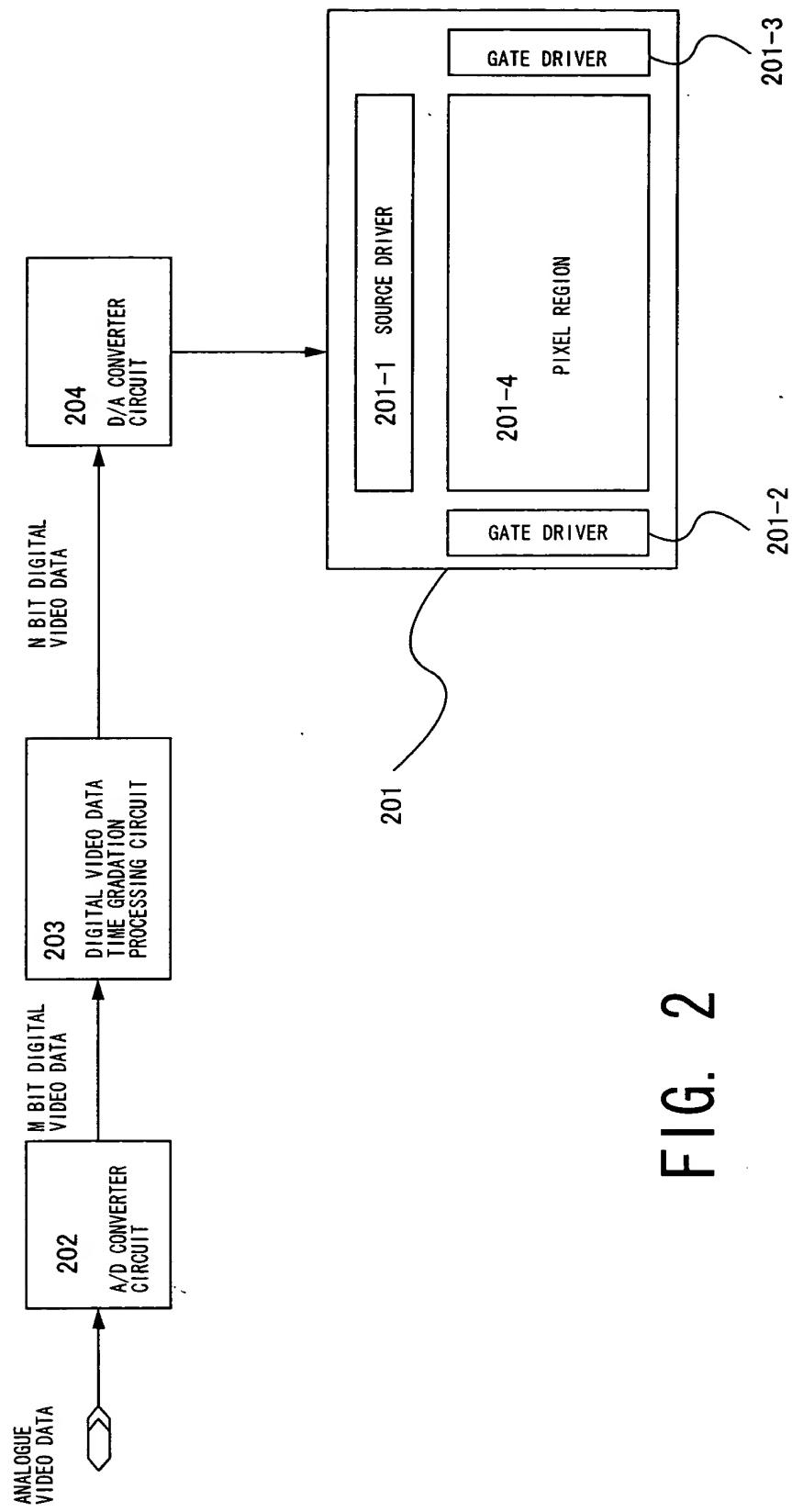


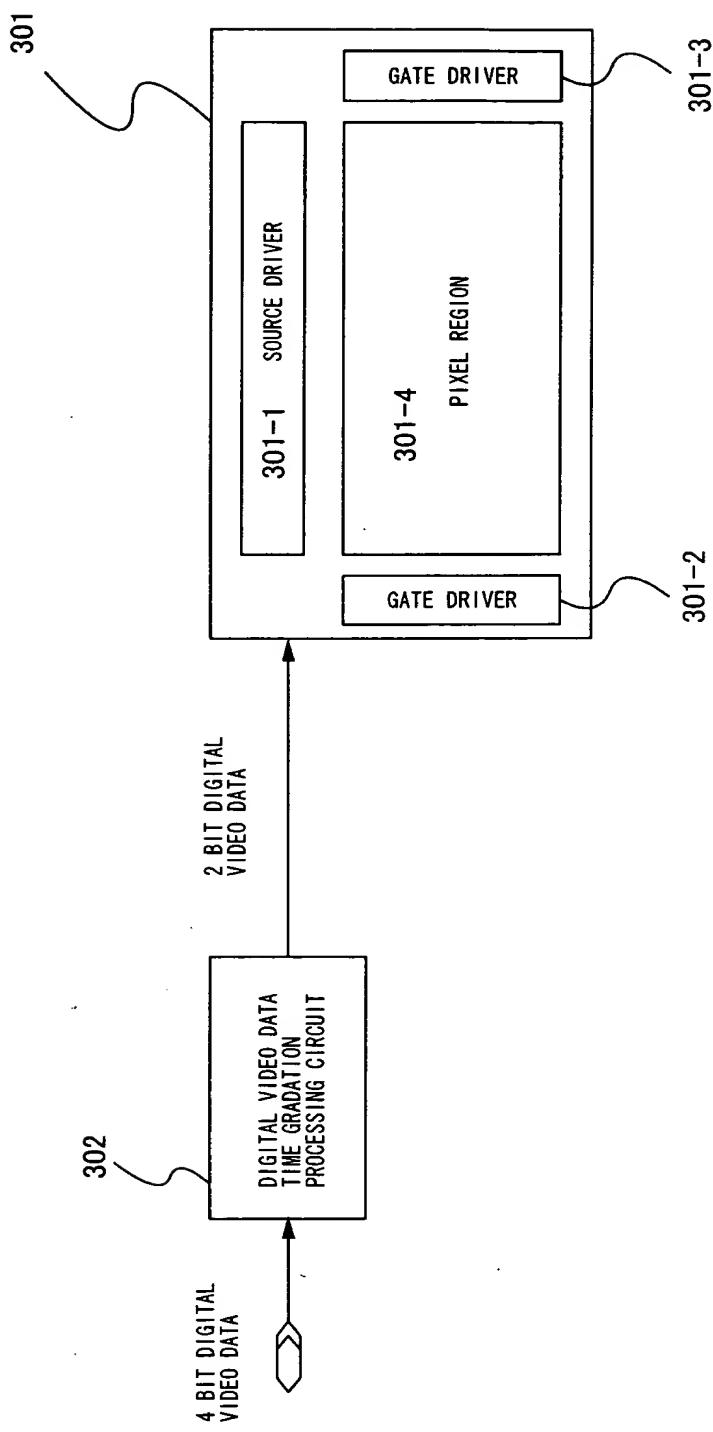
**FIG. 1**





**FIG. 2**

FIG. 3



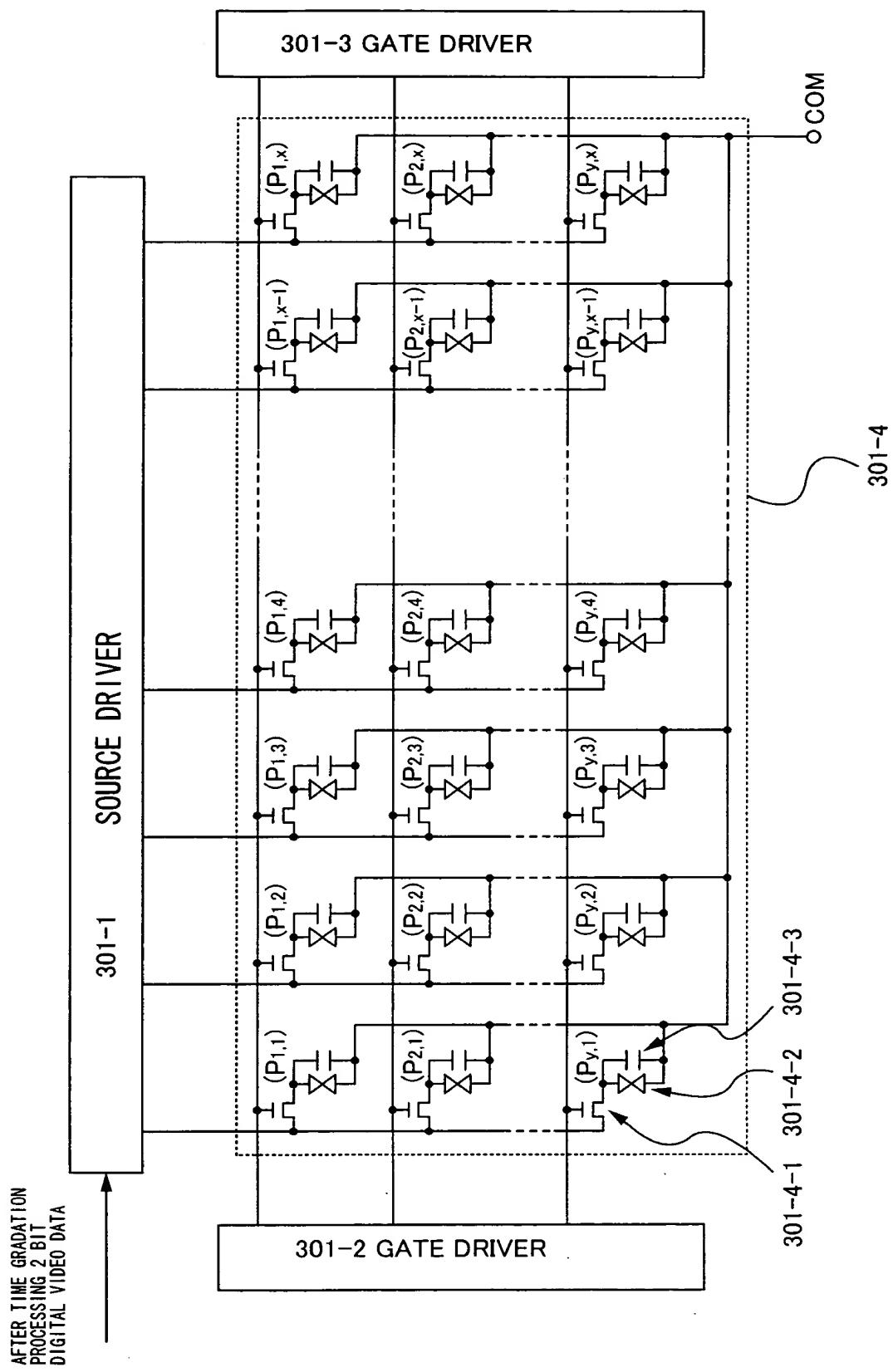


FIG. 4

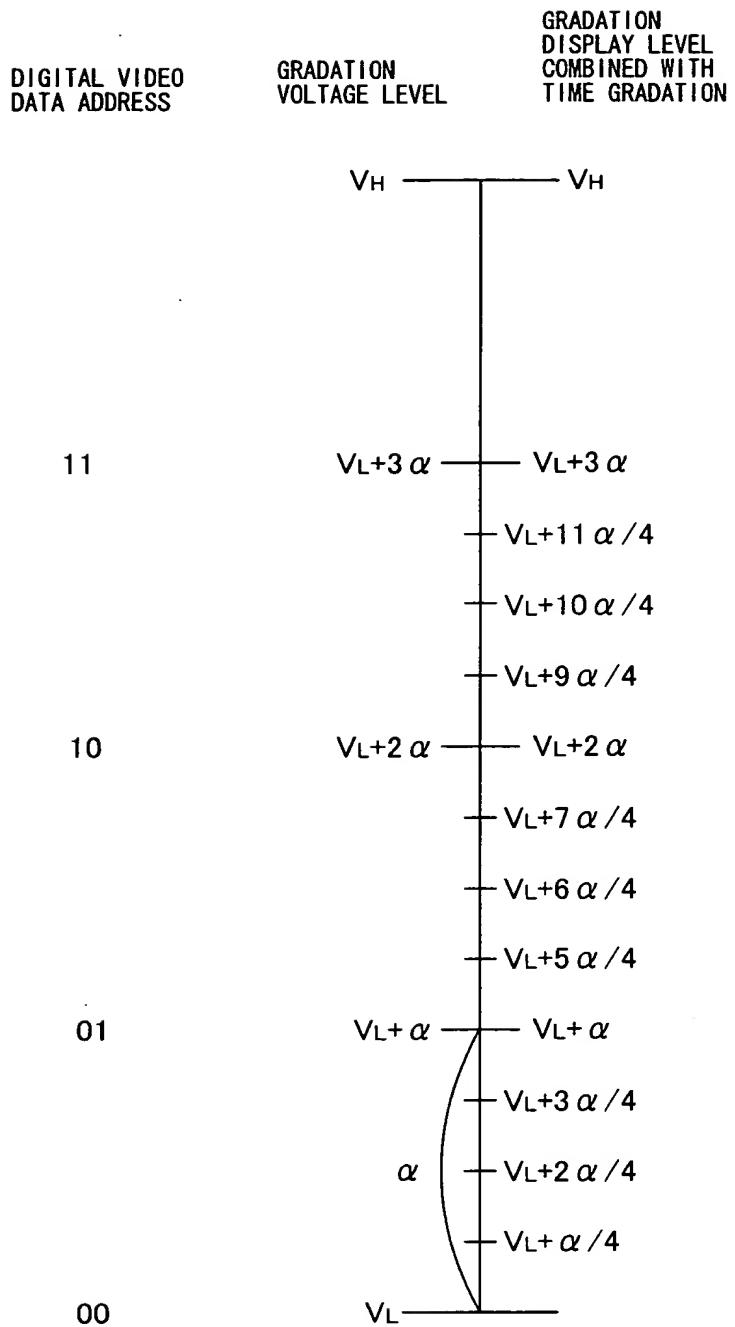
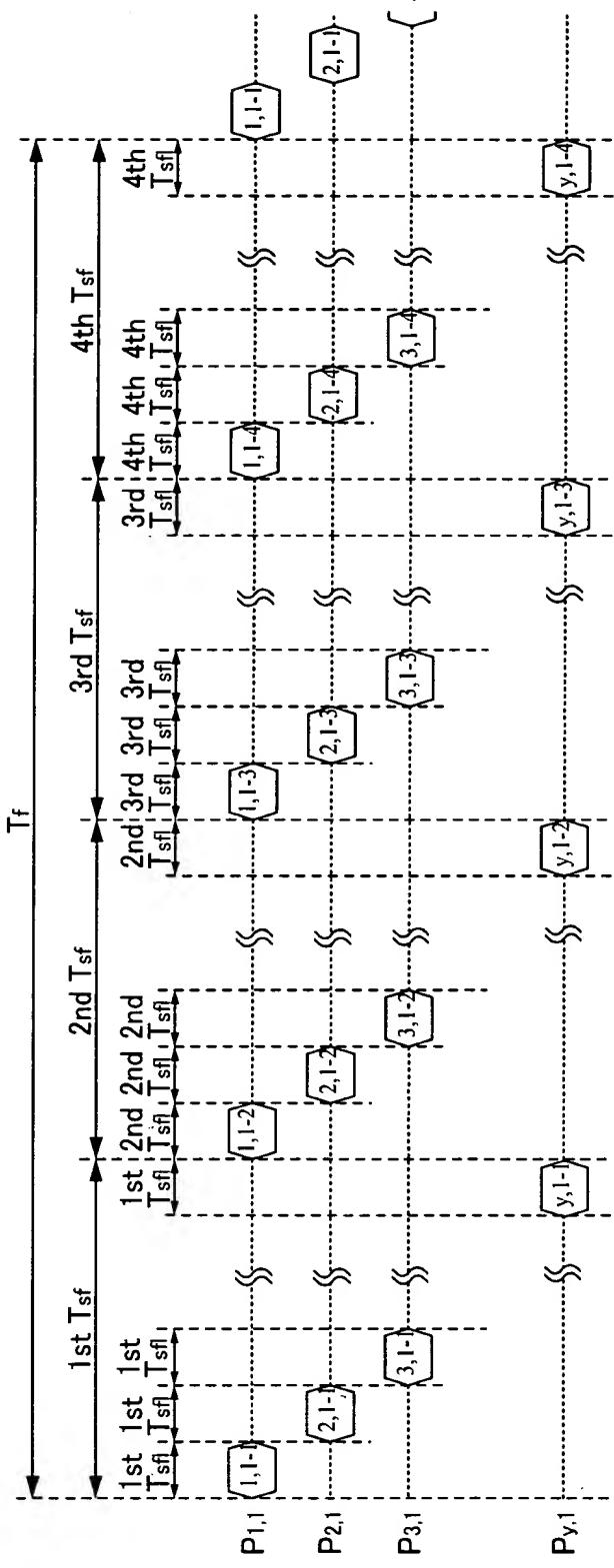


FIG. 5

FIG. 6



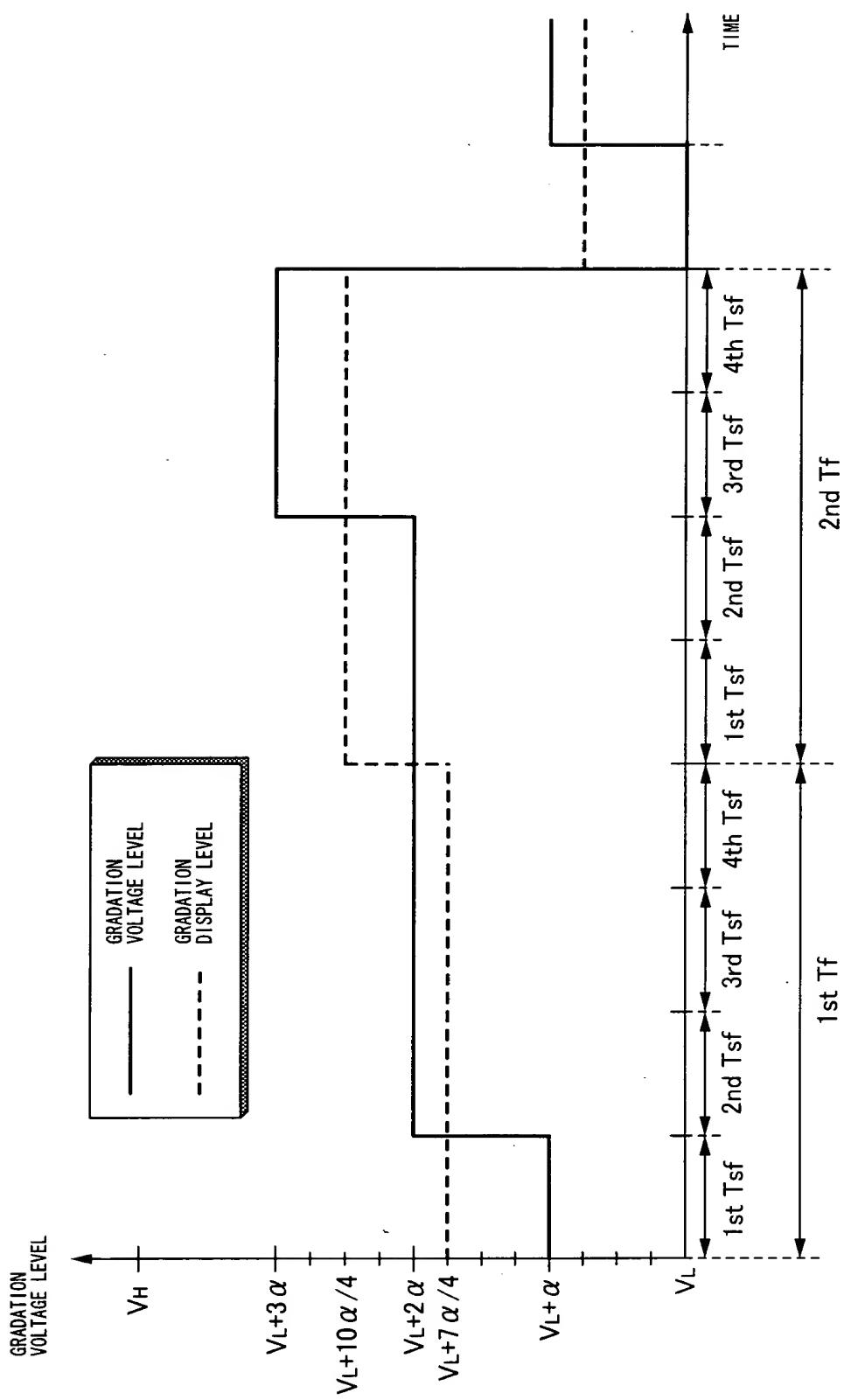
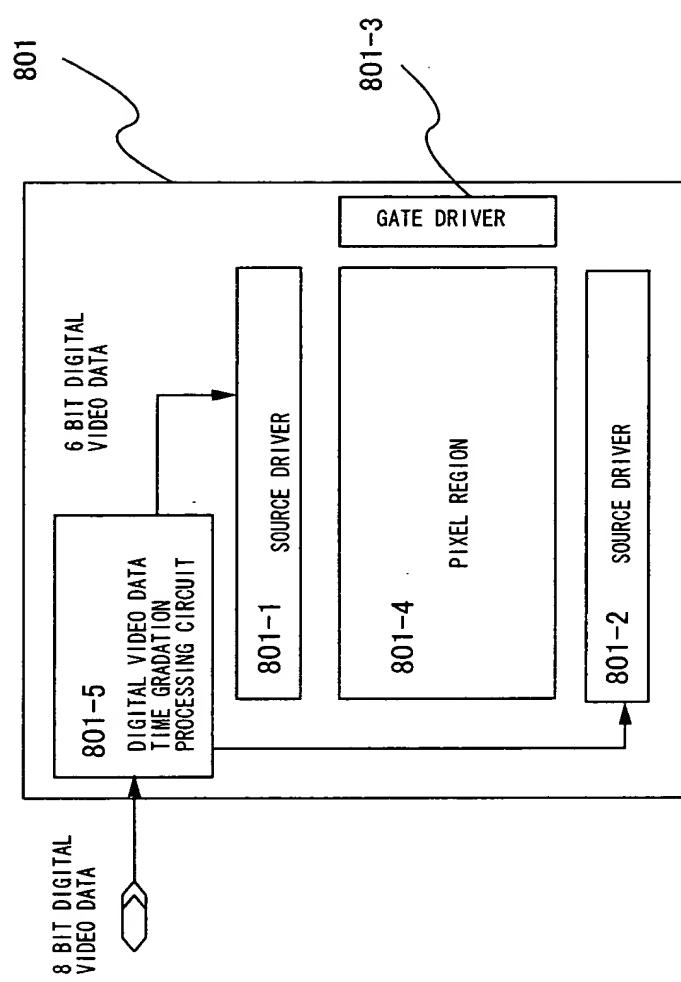


FIG. 7

**FIG. 8**



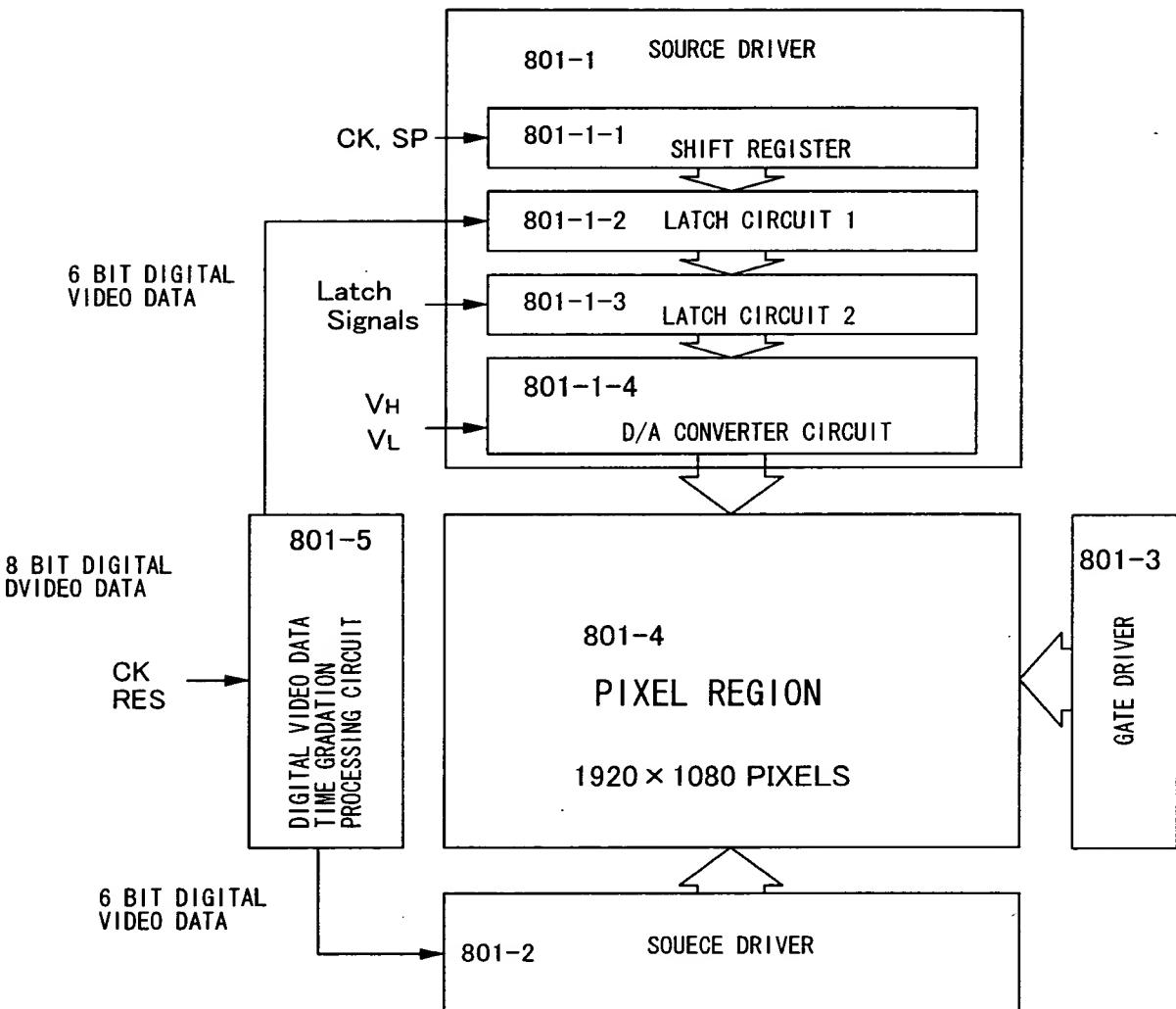


FIG. 9

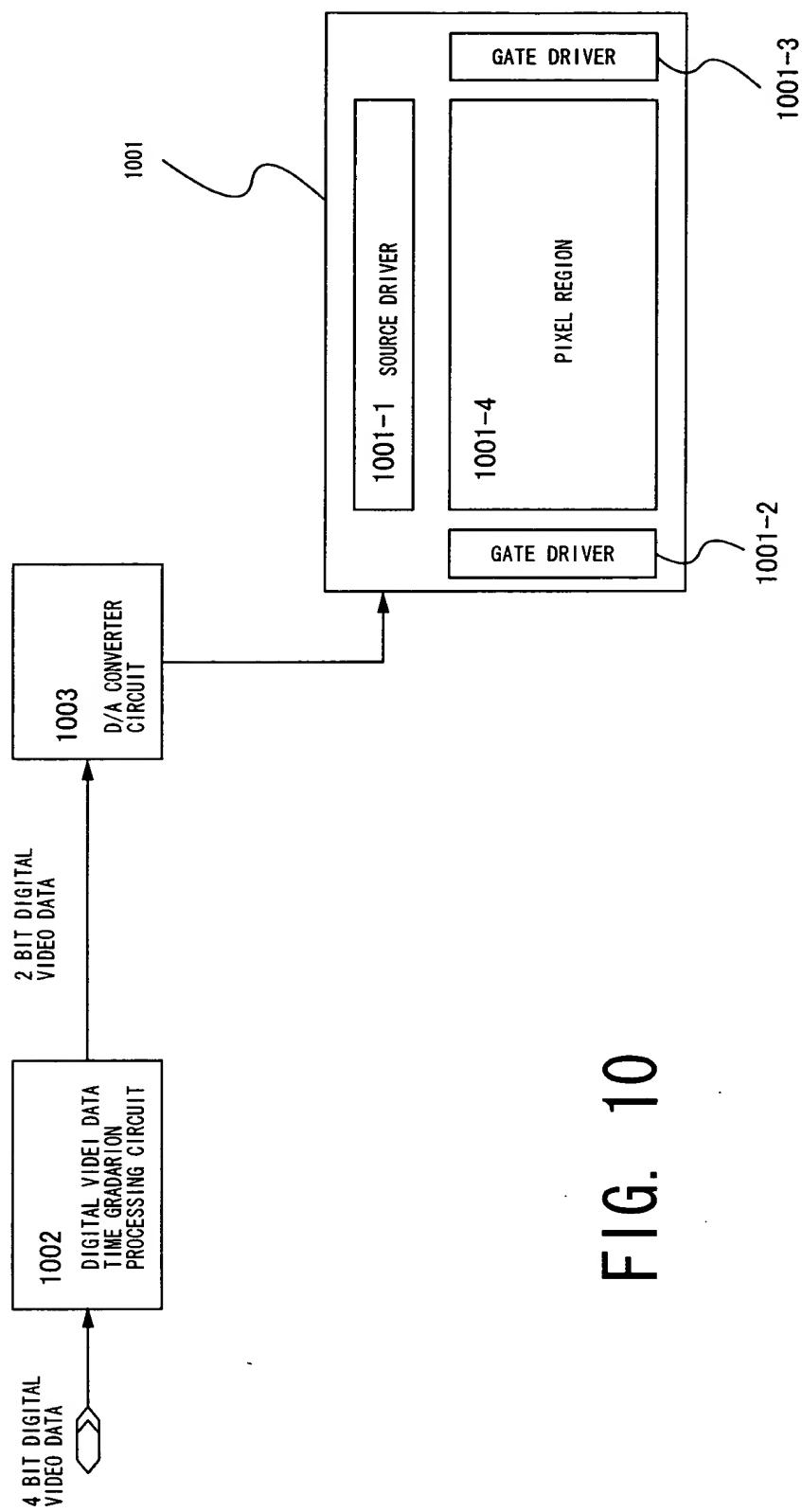
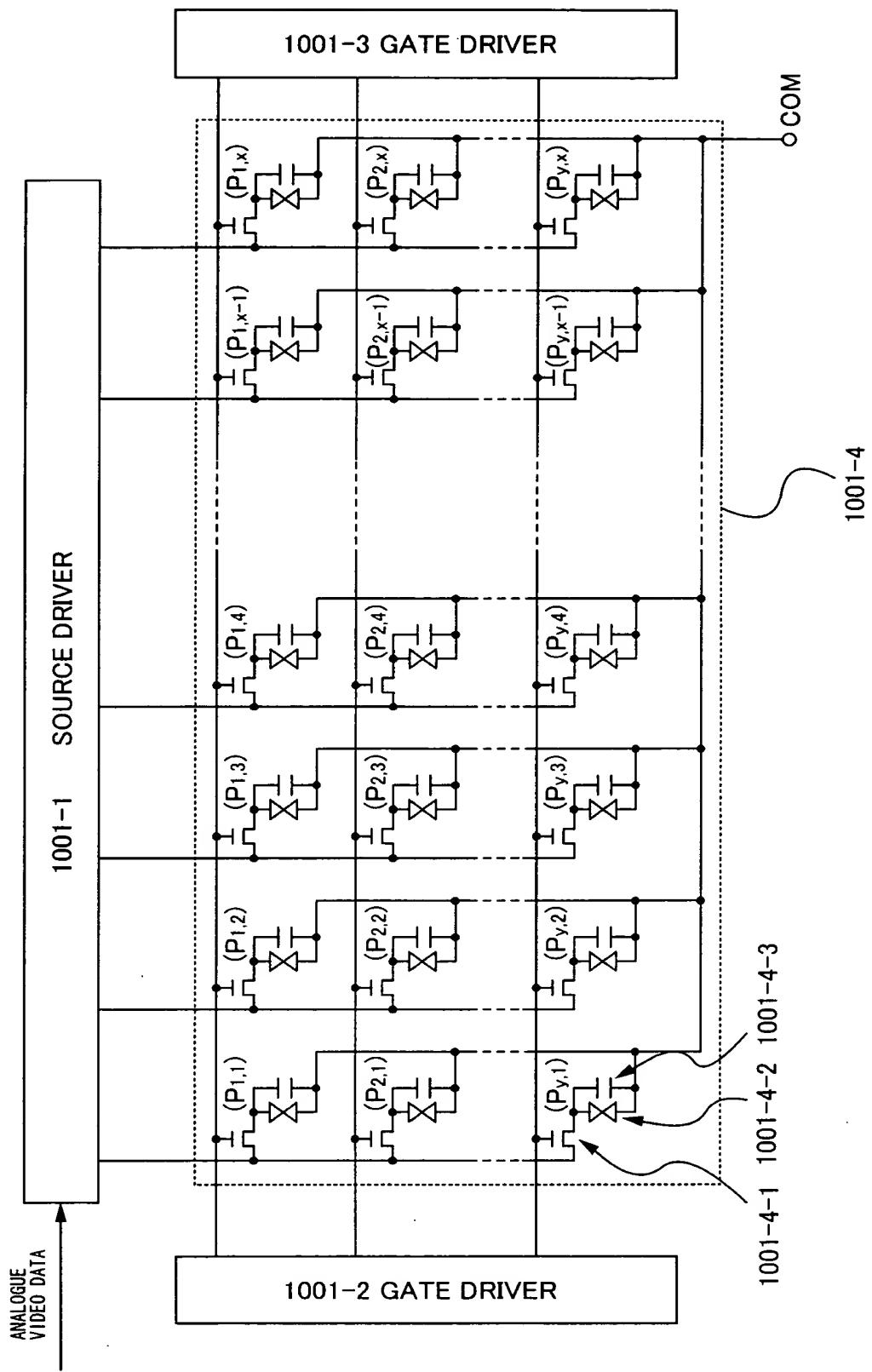
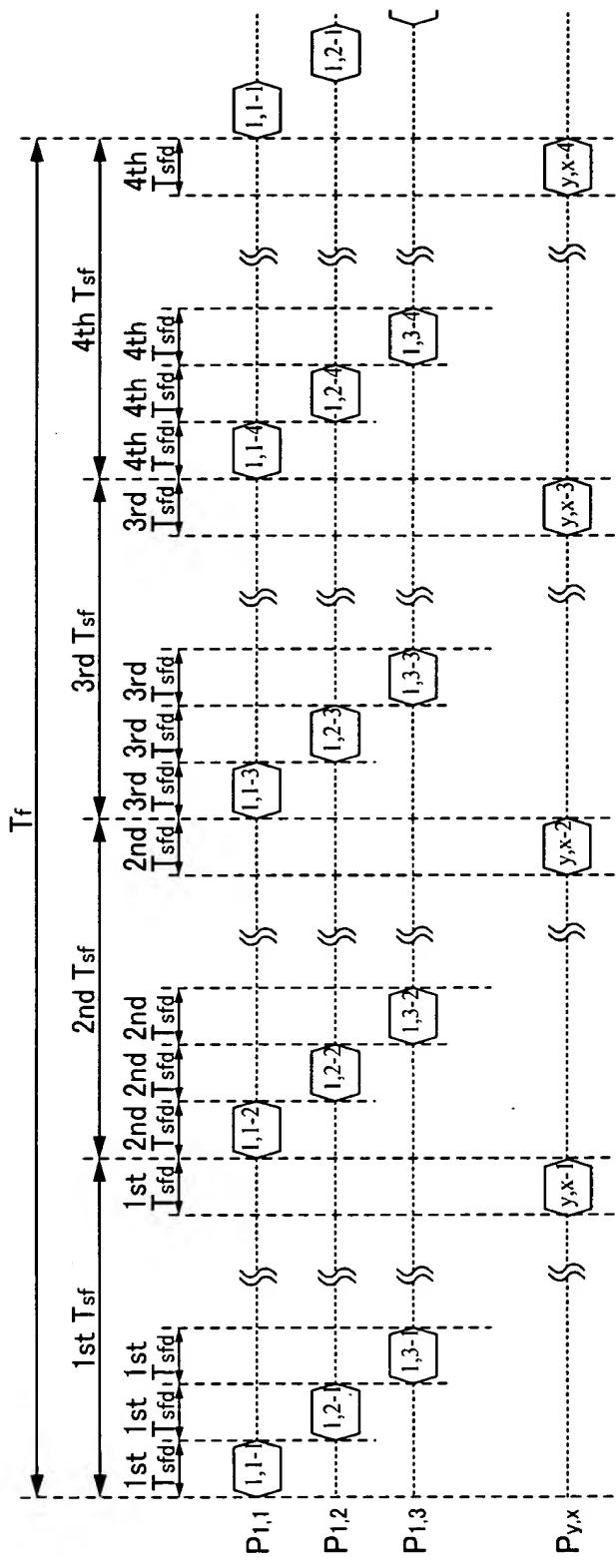


FIG. 10



**FIG. 11**

FIG. 12



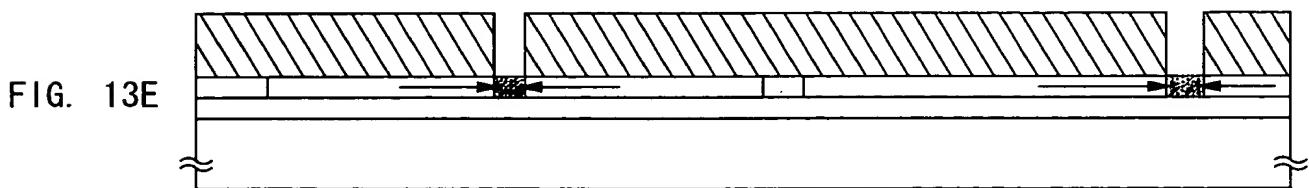
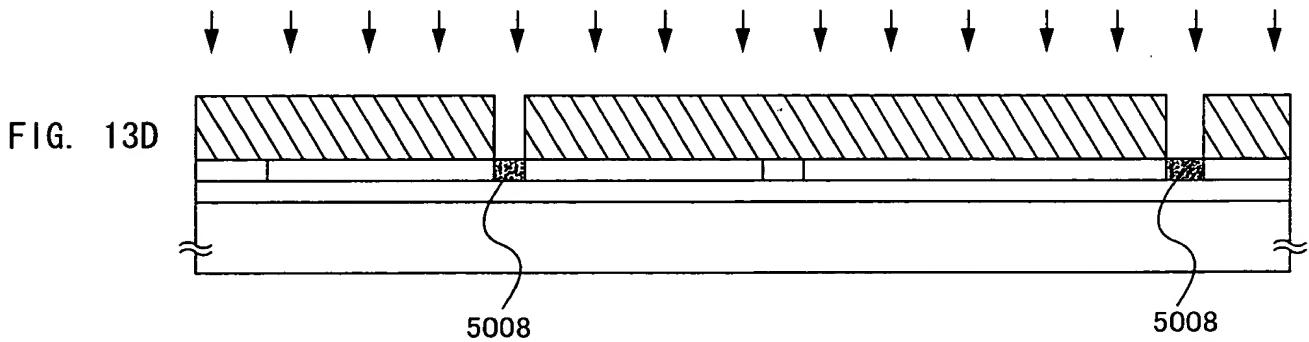
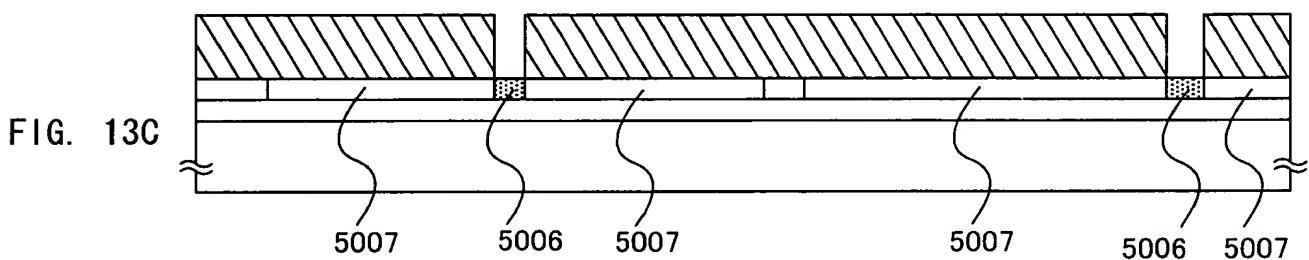
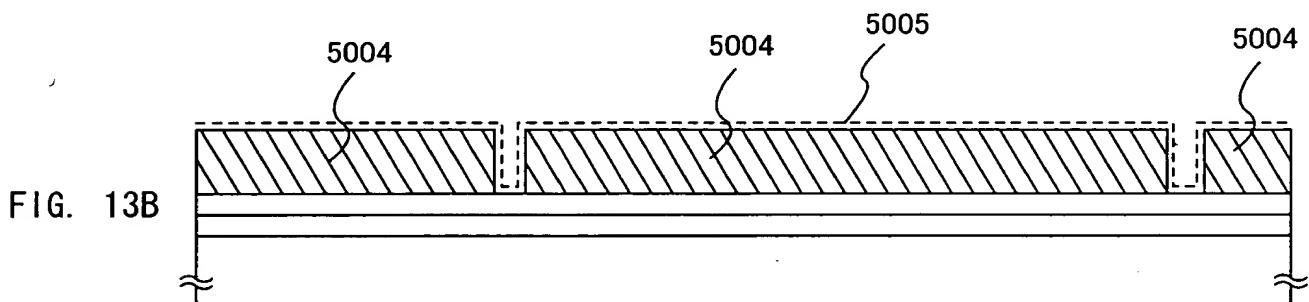
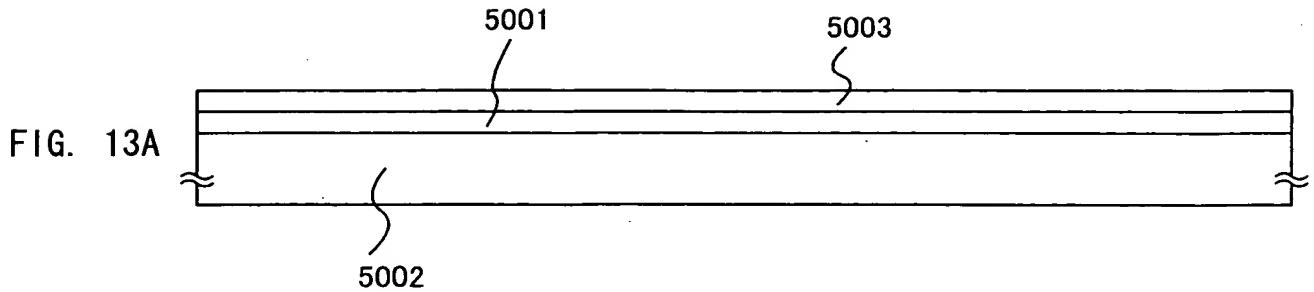


FIG. 14A

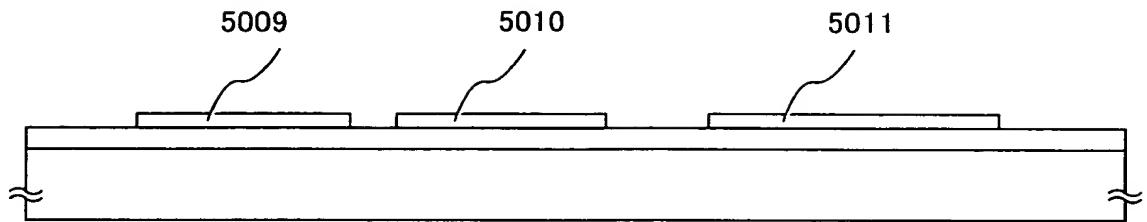


FIG. 14B

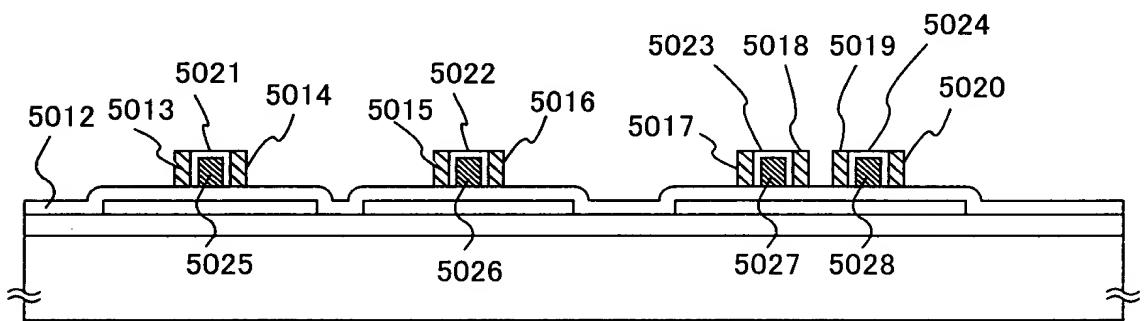


FIG. 14C

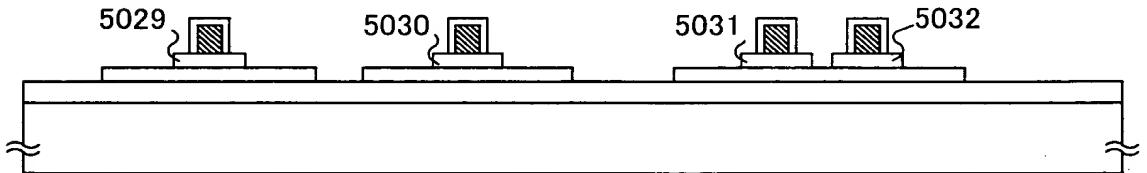


FIG. 15A

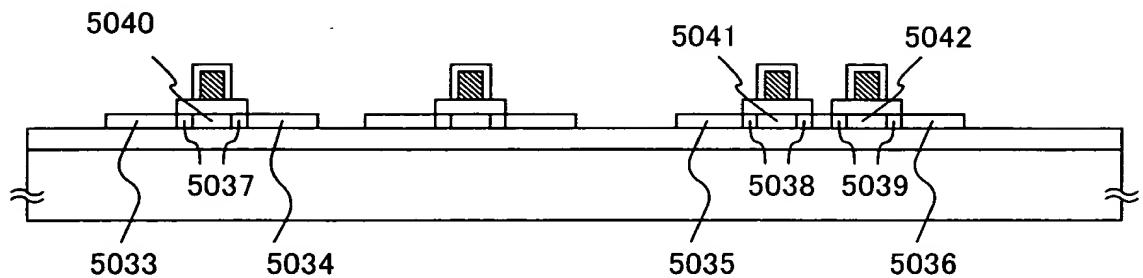


FIG. 15B

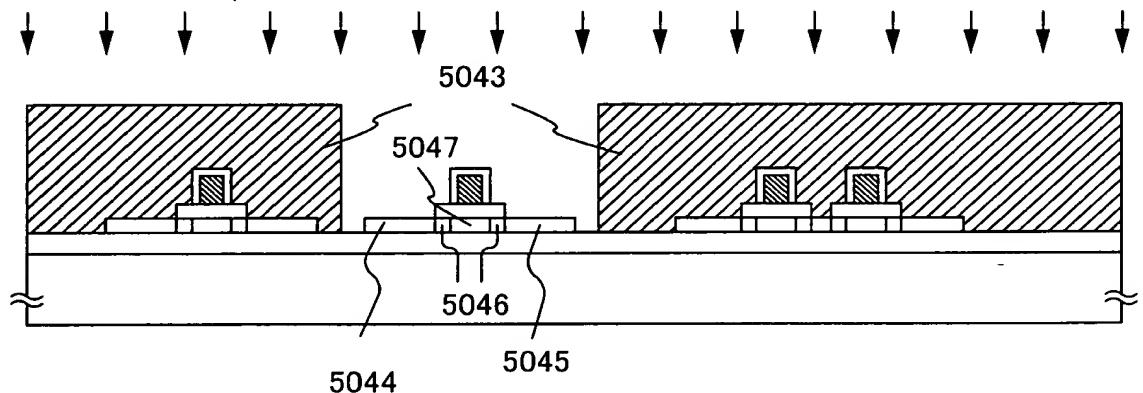


FIG. 15C

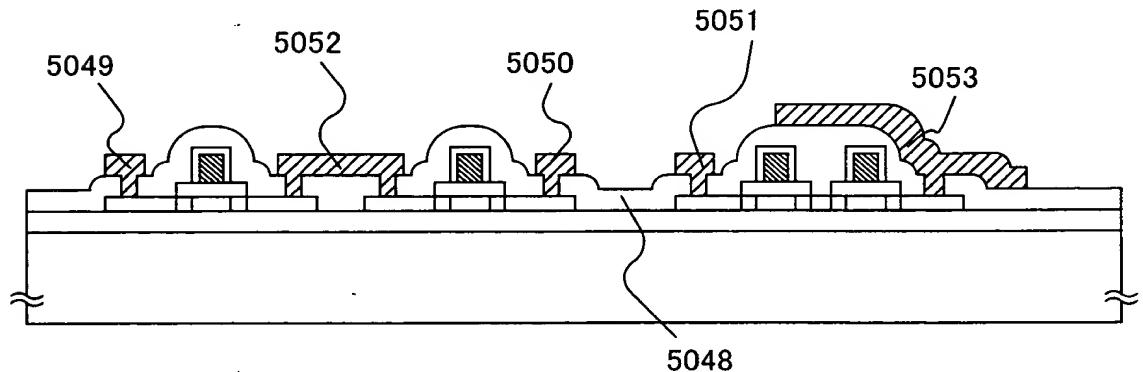


FIG. 16A

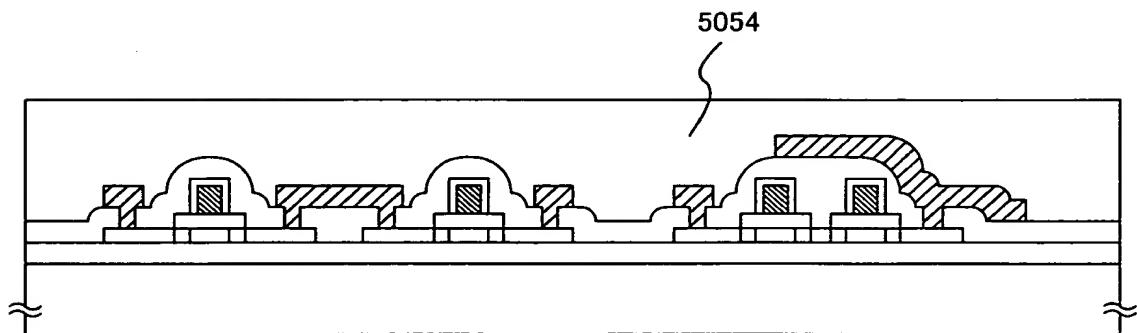


FIG. 16B

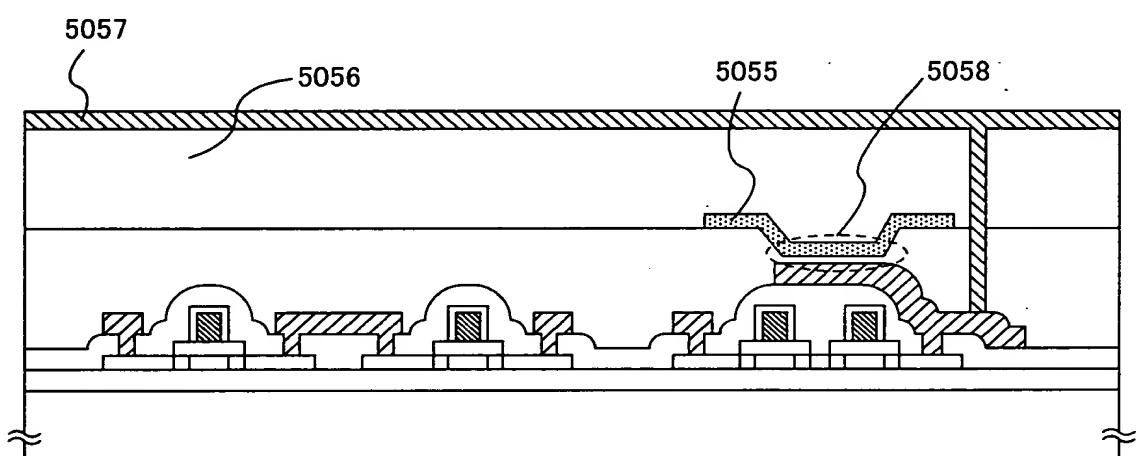
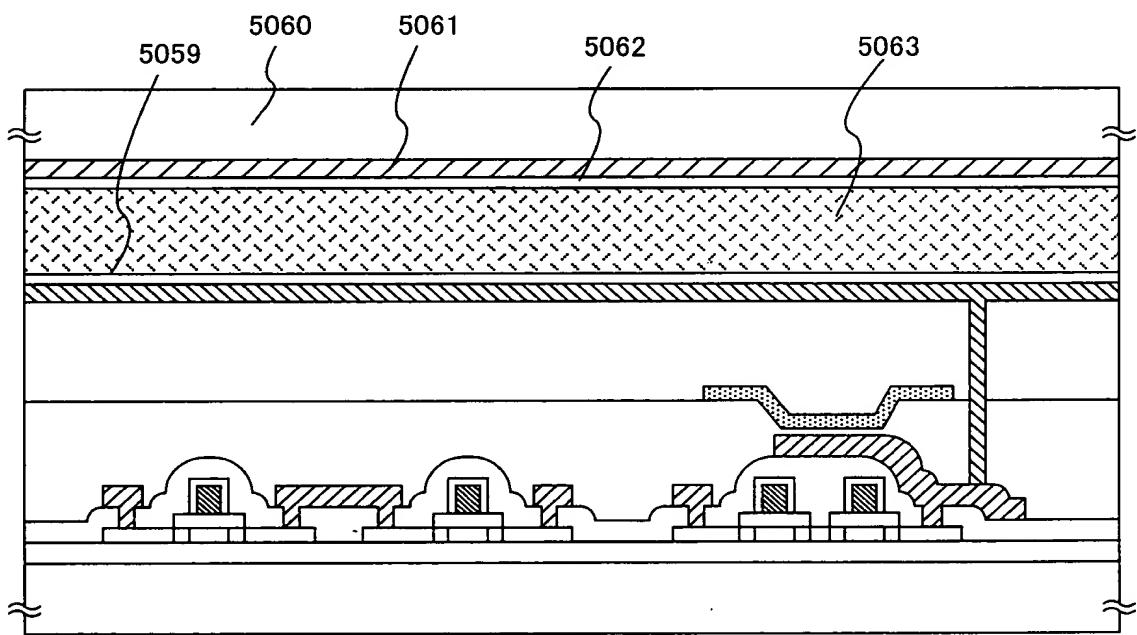


FIG. 16C



00000000000000000000000000000000

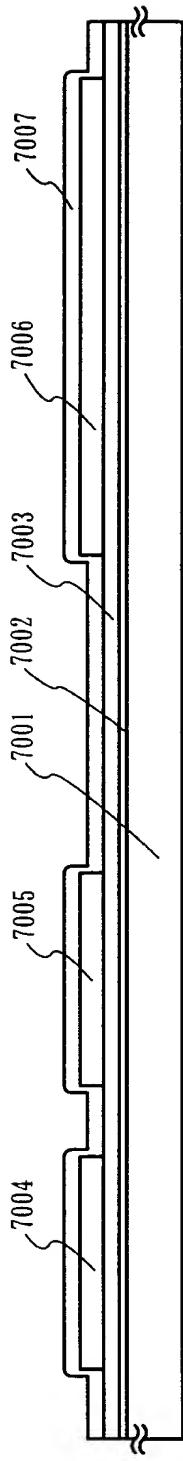


FIG. 17A

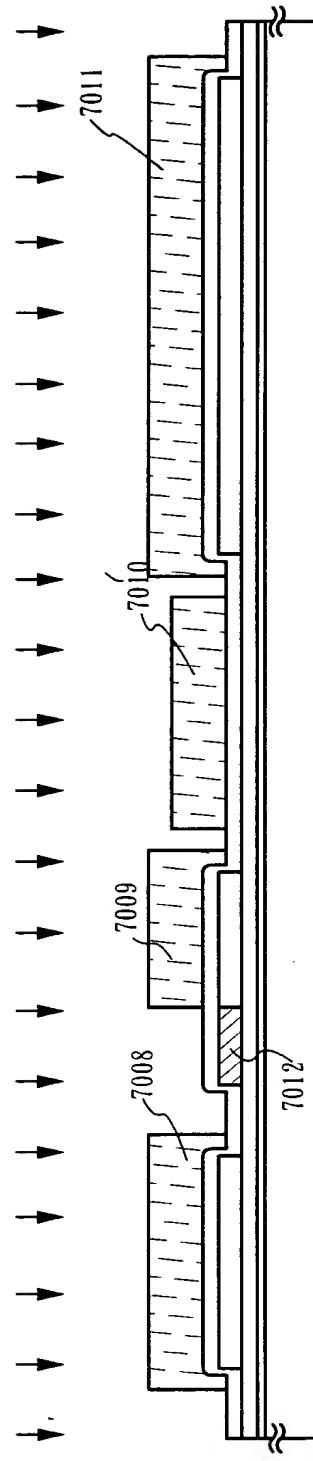


FIG. 17B

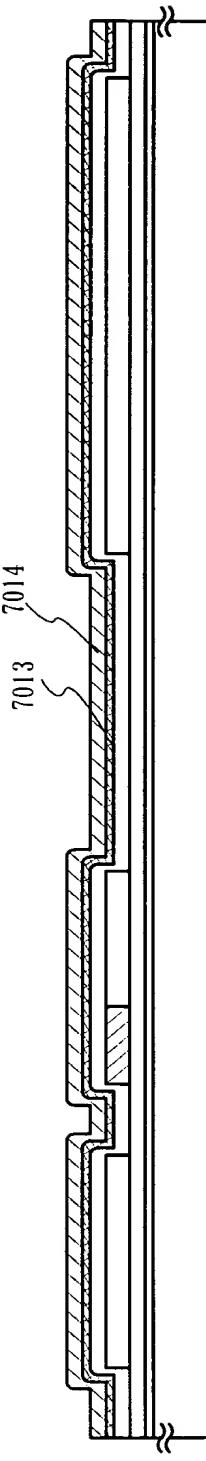


FIG. 17C

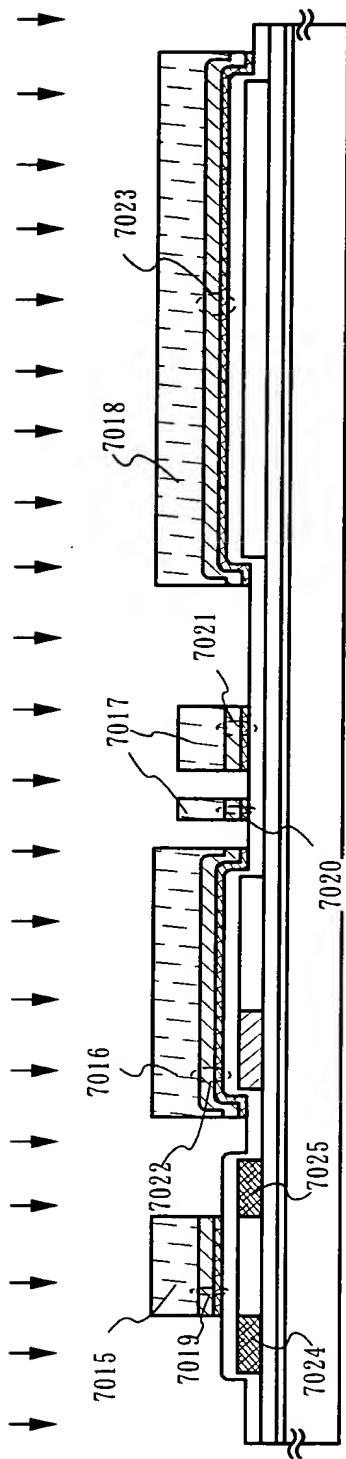


FIG. 18A

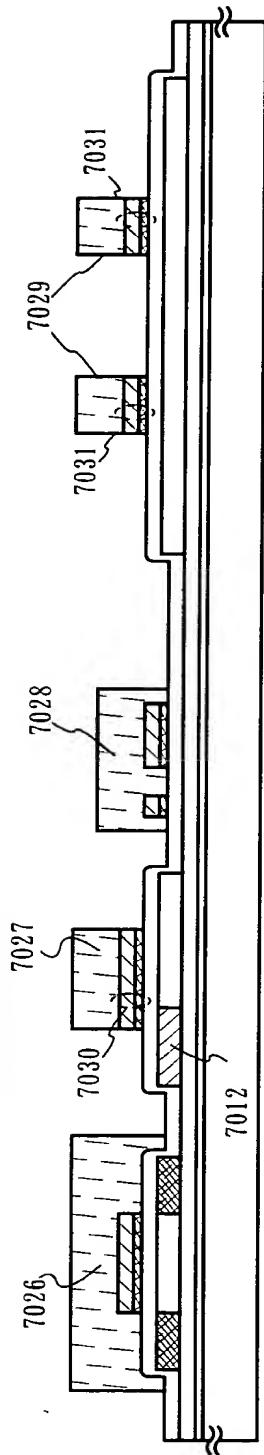


FIG. 18B

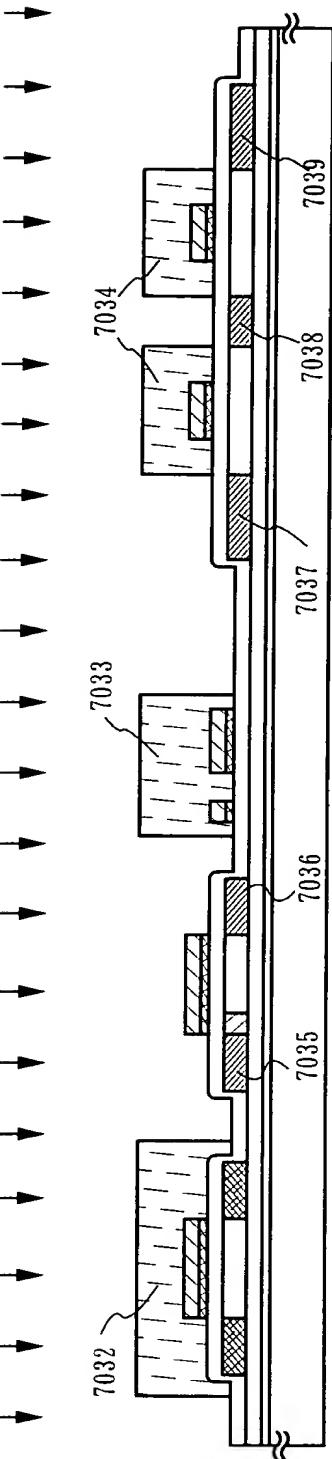
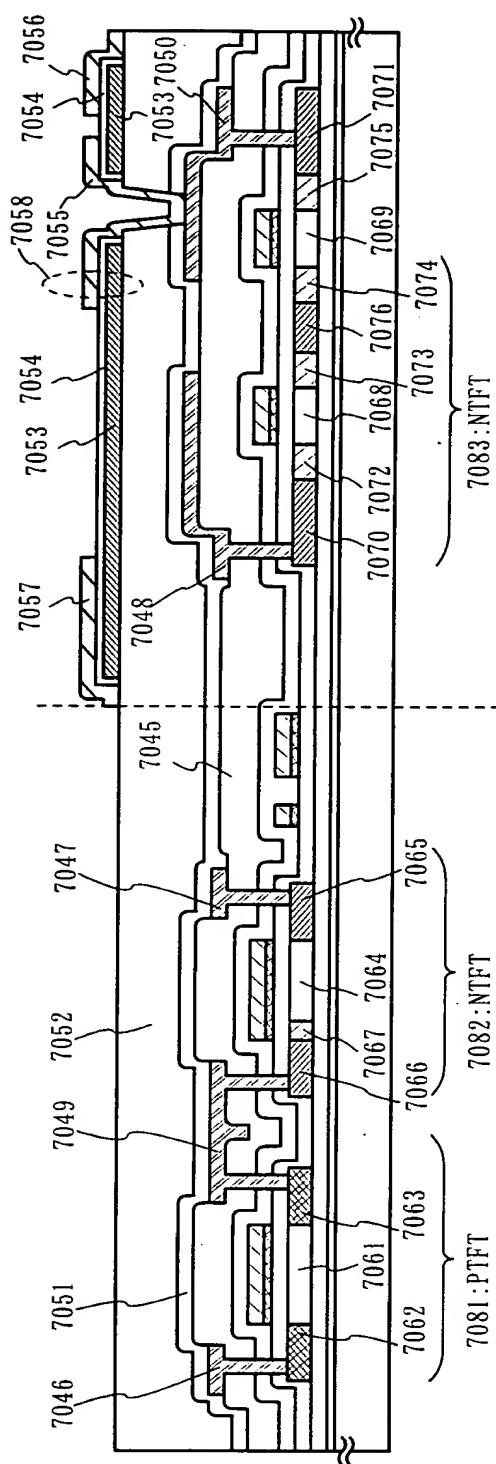
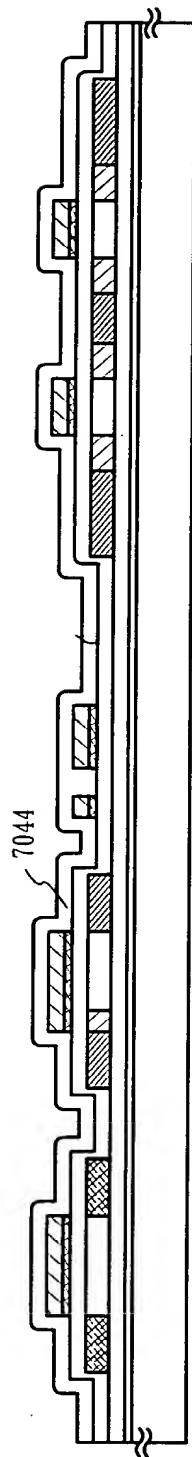
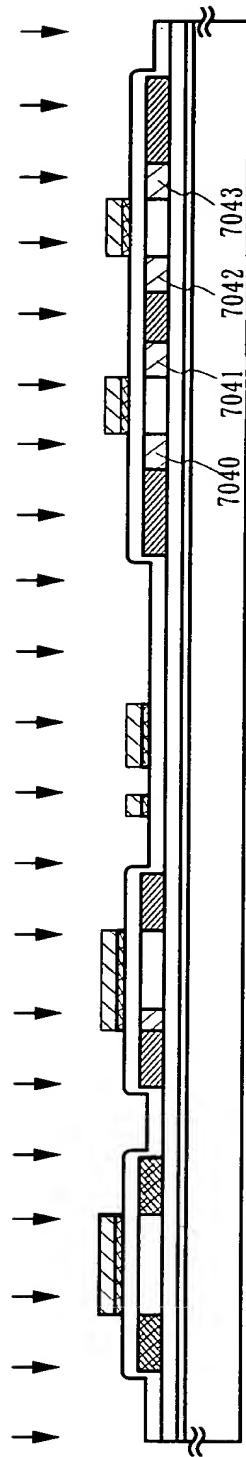


FIG. 18C

புது தெரு போக்குவரத்து நிலைமை



CMOS CIRCUIT

PIXEL REGION

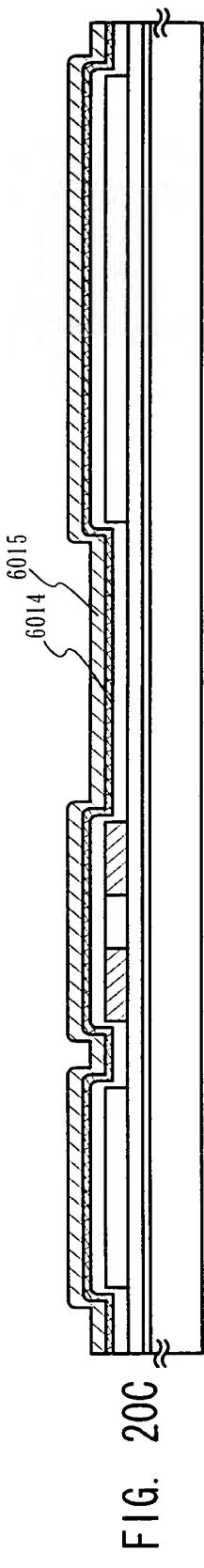
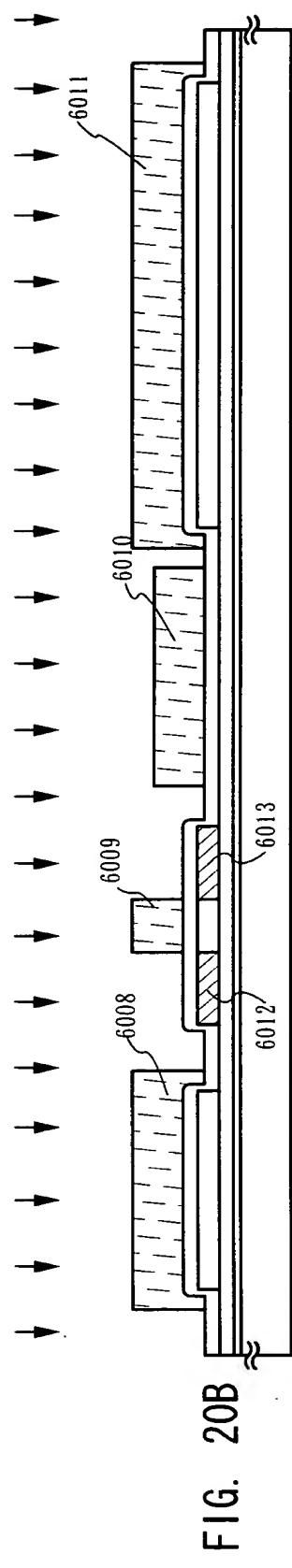
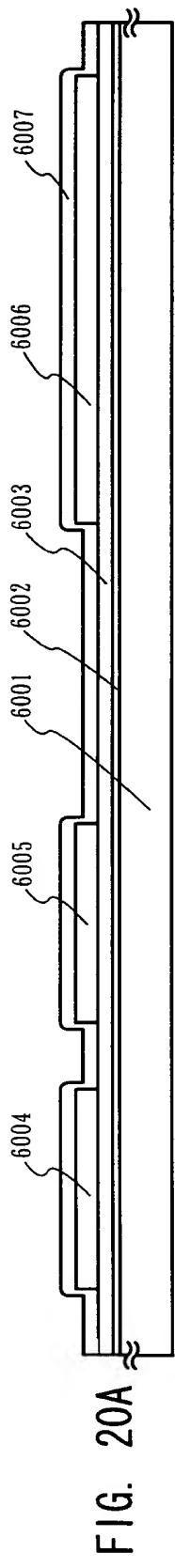


FIG. 21A

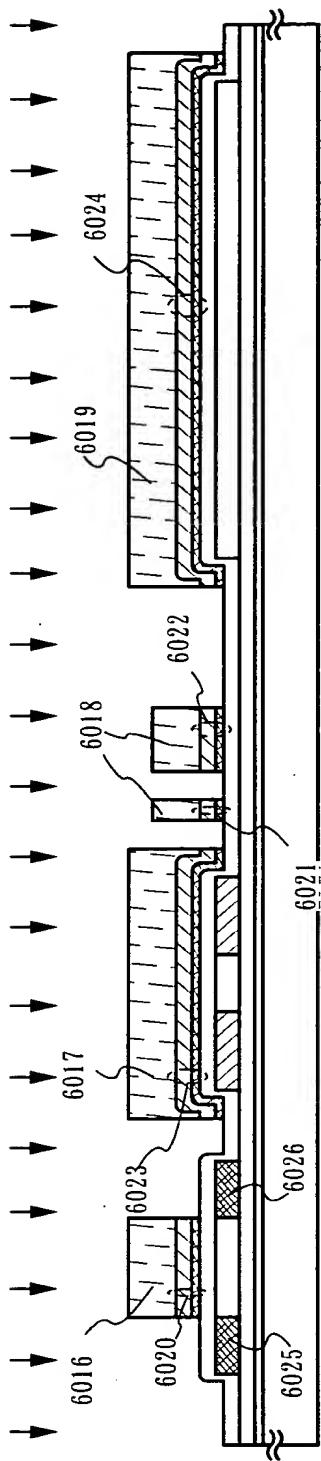


FIG. 21A

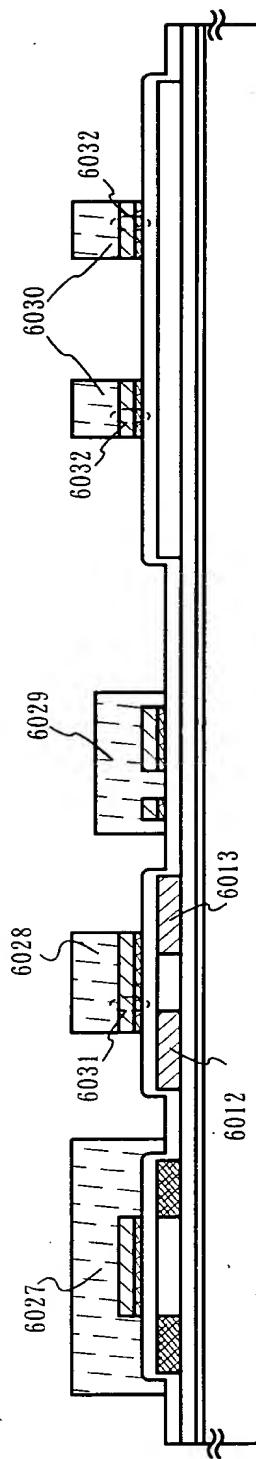


FIG. 21B

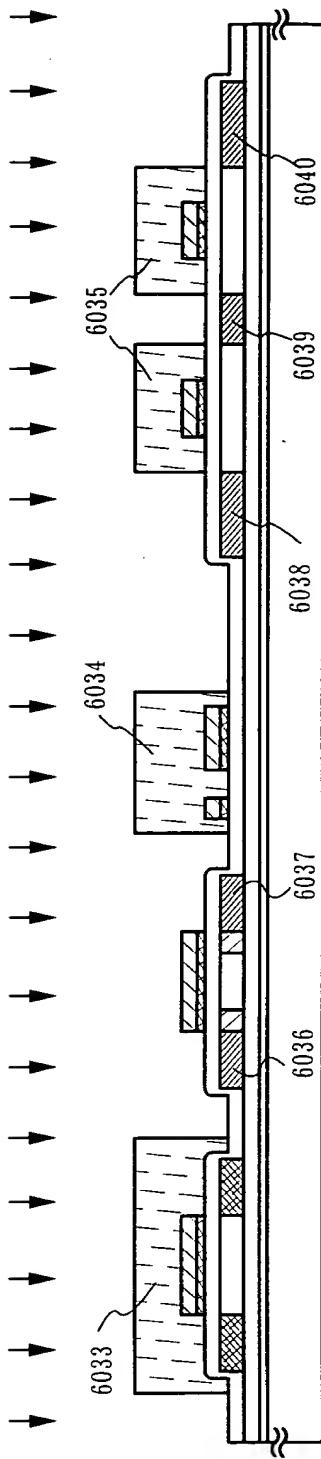
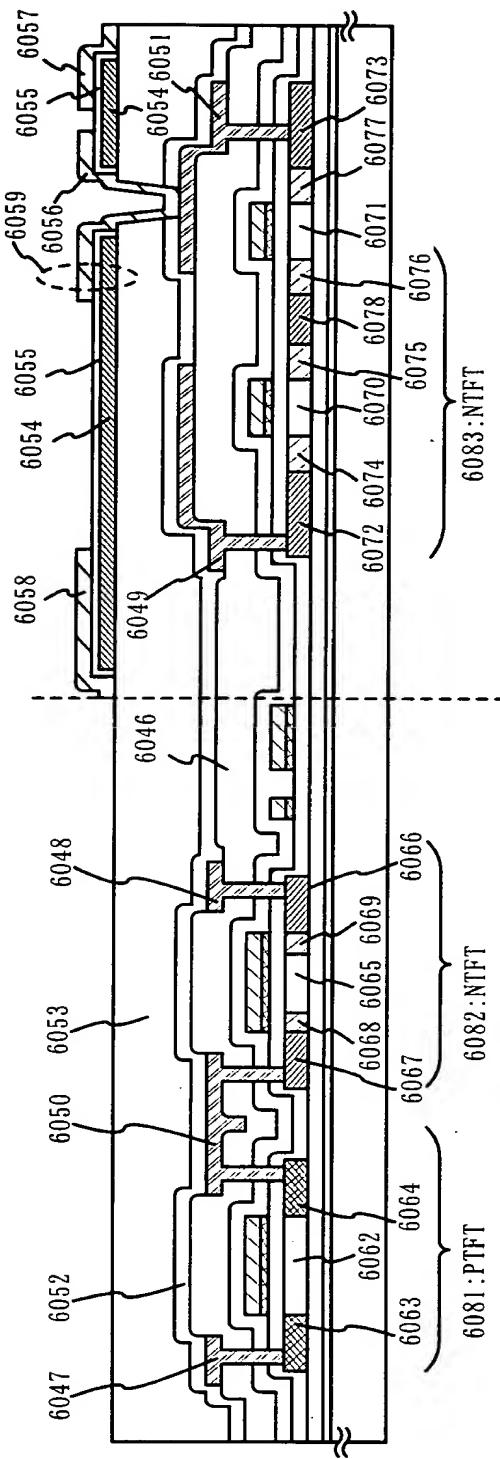
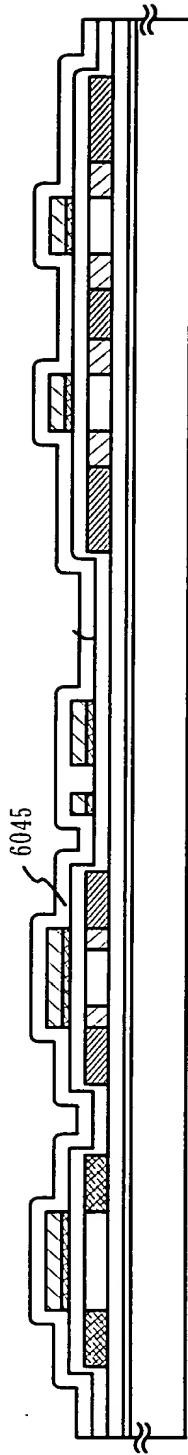
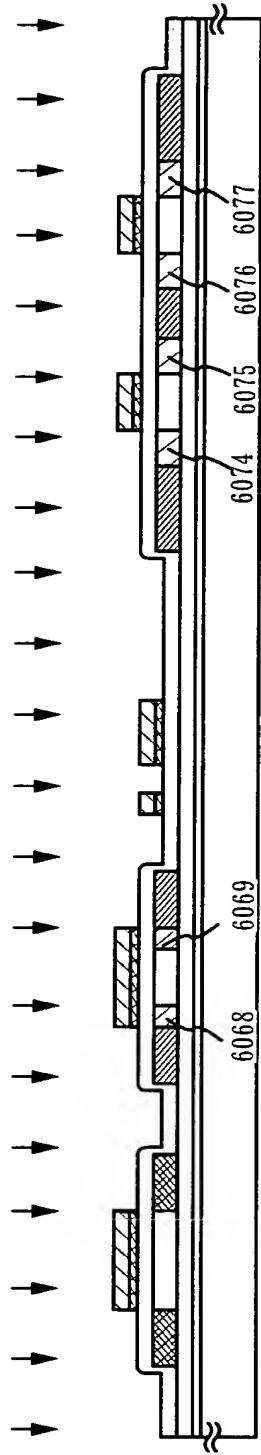


FIG. 21C



CMOS CIRCUIT      PIXEL REGION

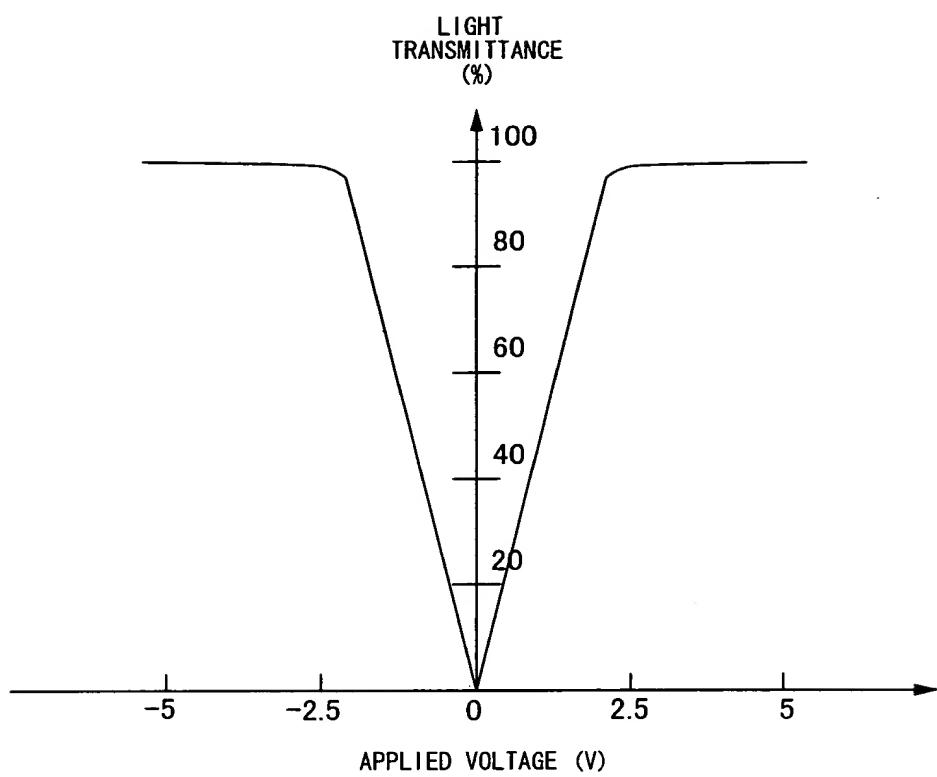
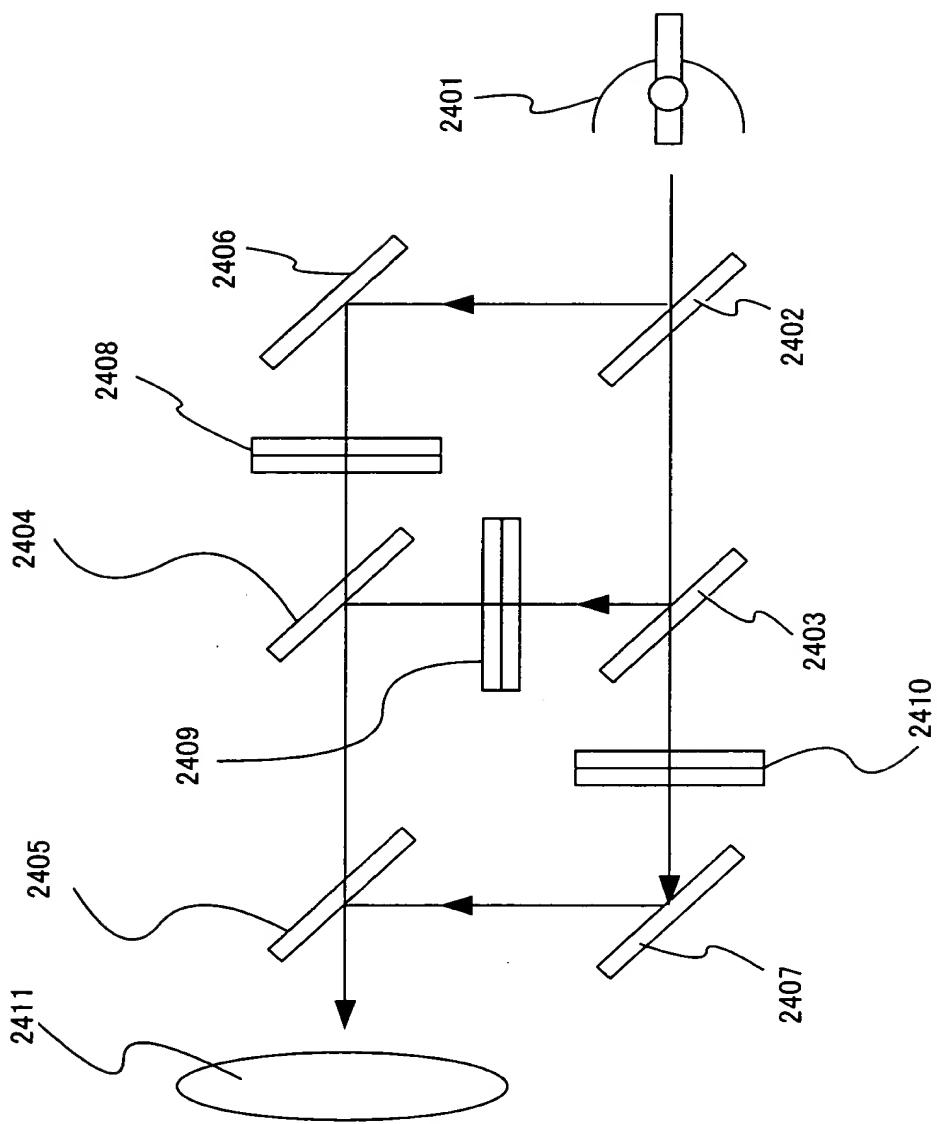


FIG. 23

FIG. 24



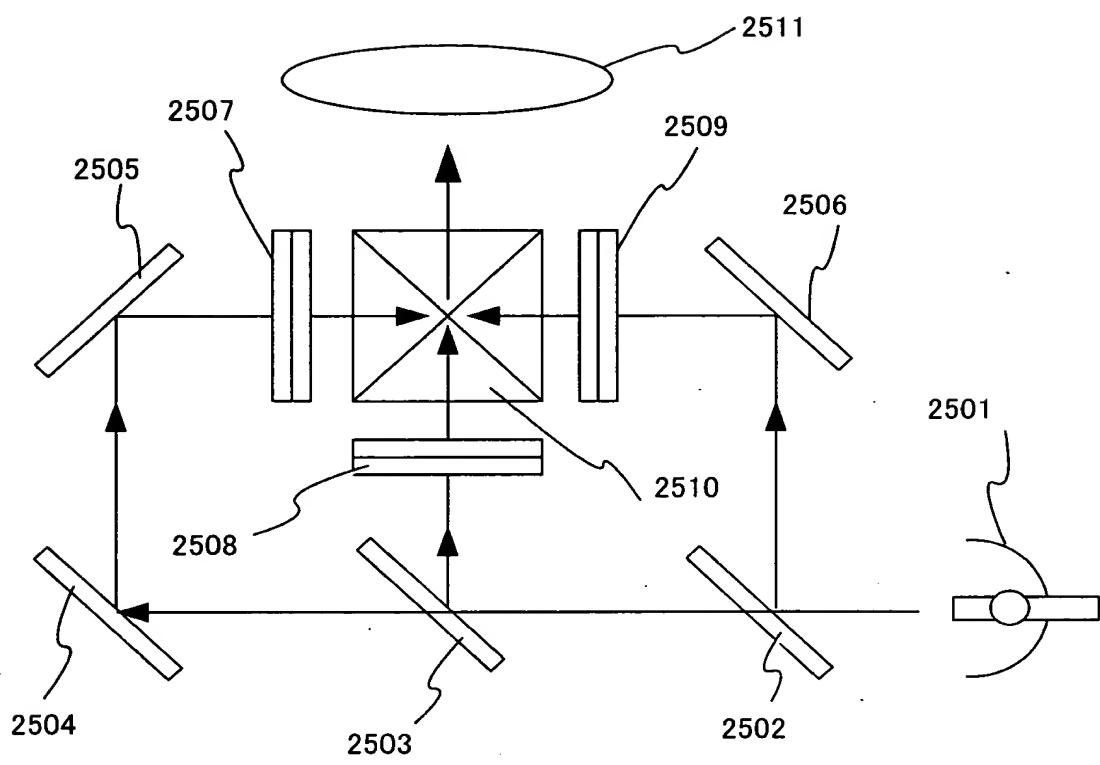


FIG. 25

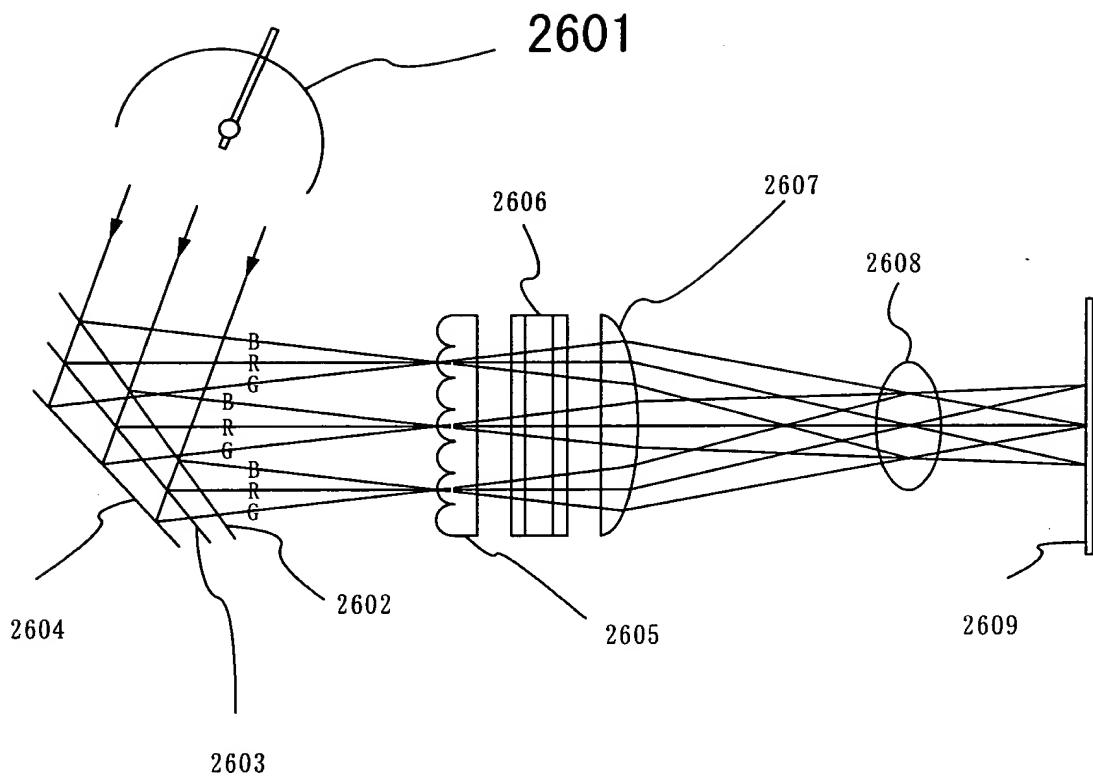
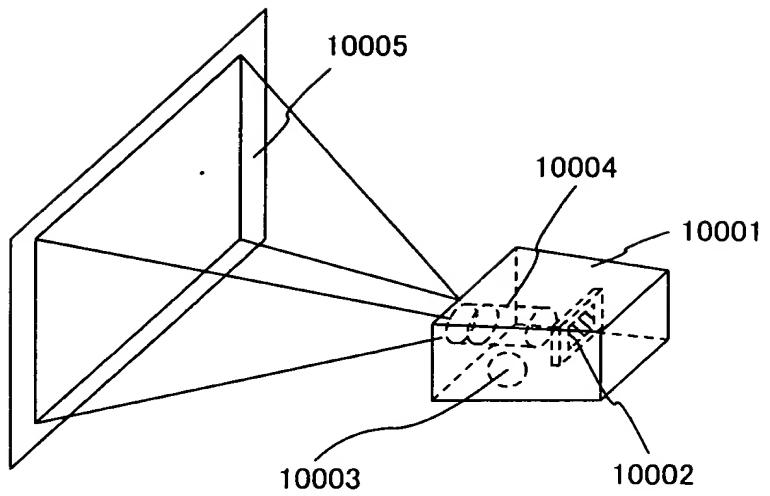
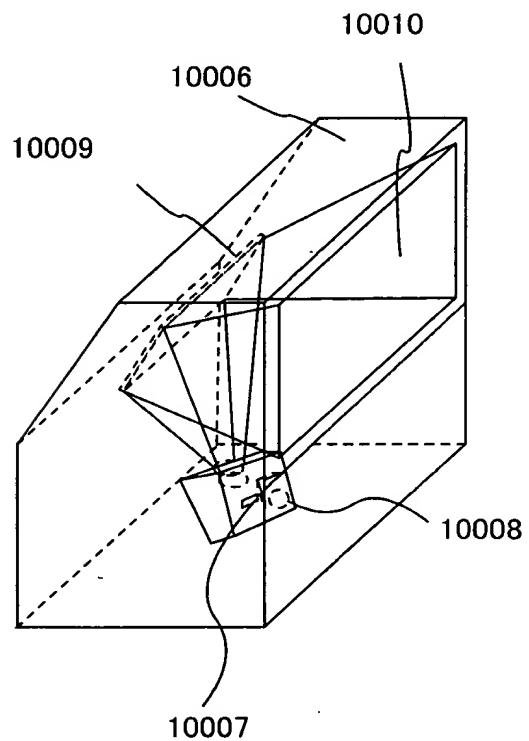


FIG. 26



**FIG. 27A**

20200722202520200560



**FIG. 27B**

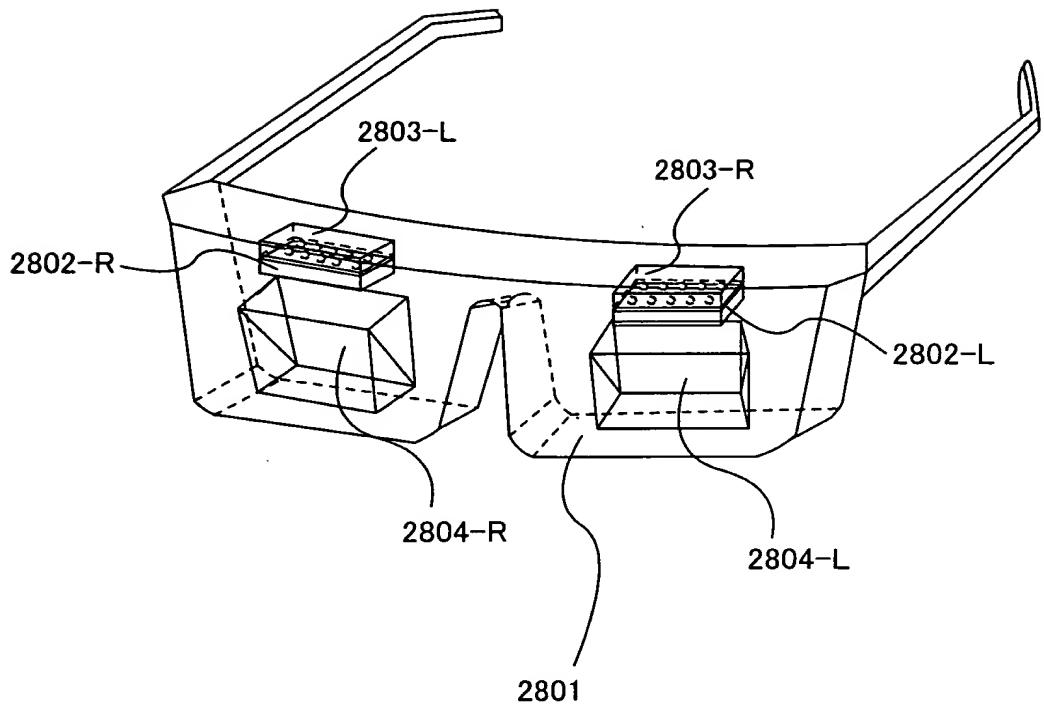


FIG. 28

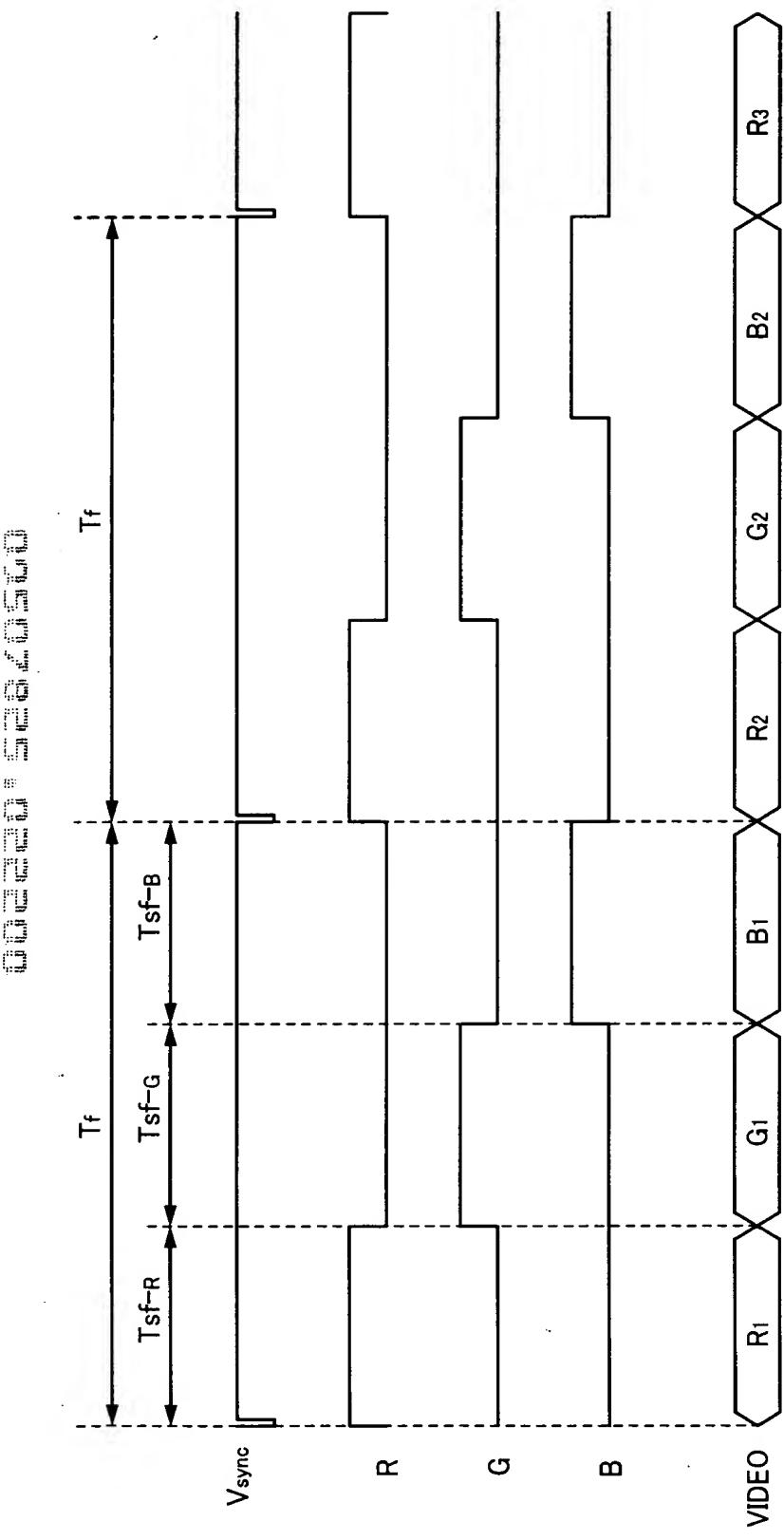


FIG. 29

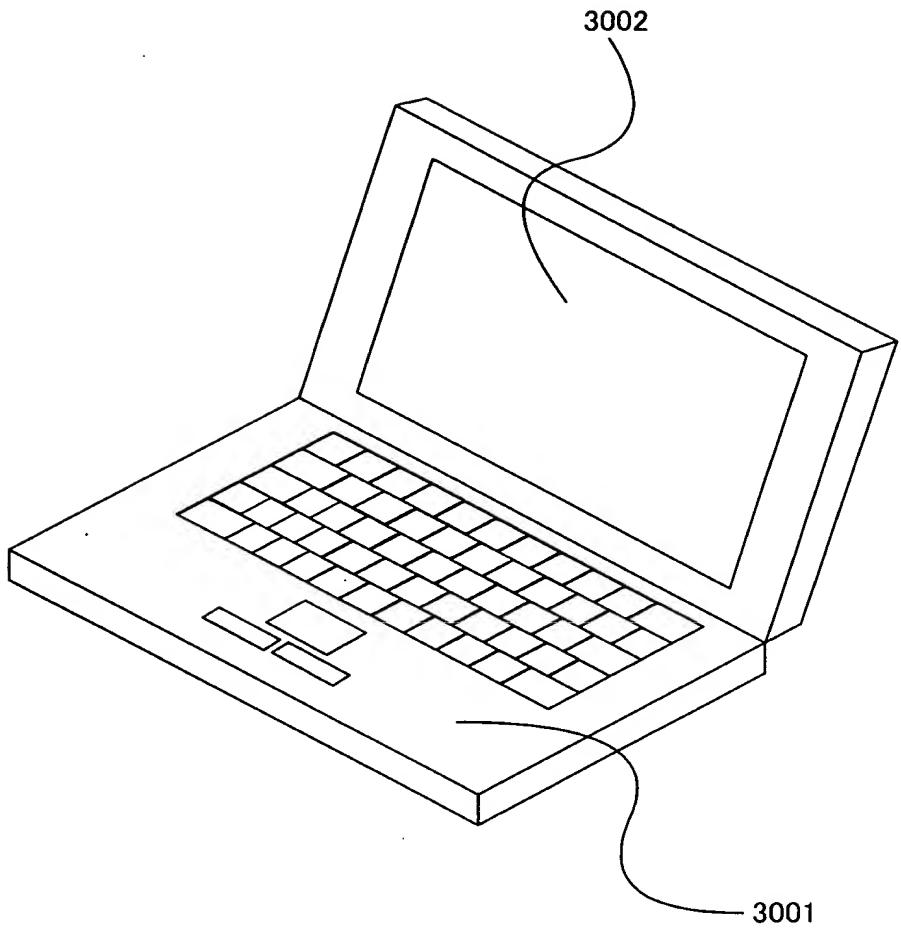


FIG. 30

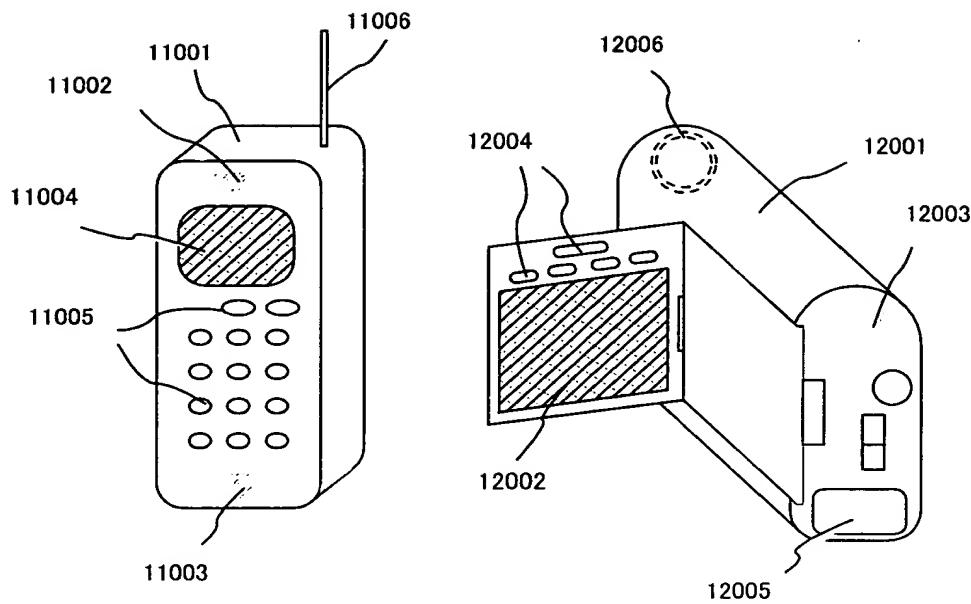


FIG. 31A

FIG. 31B

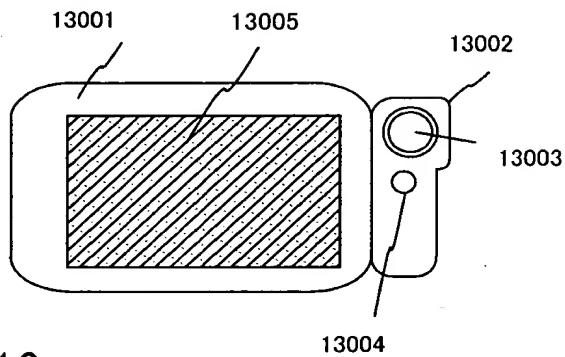


FIG. 31C

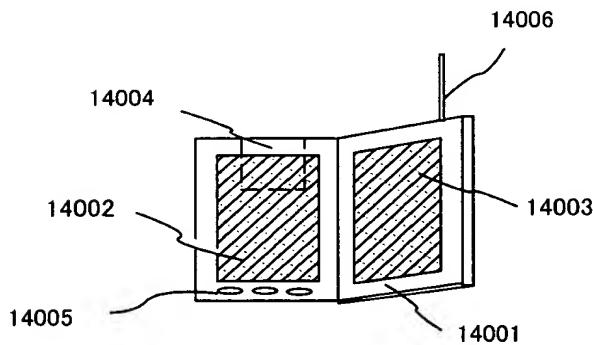


FIG. 31D

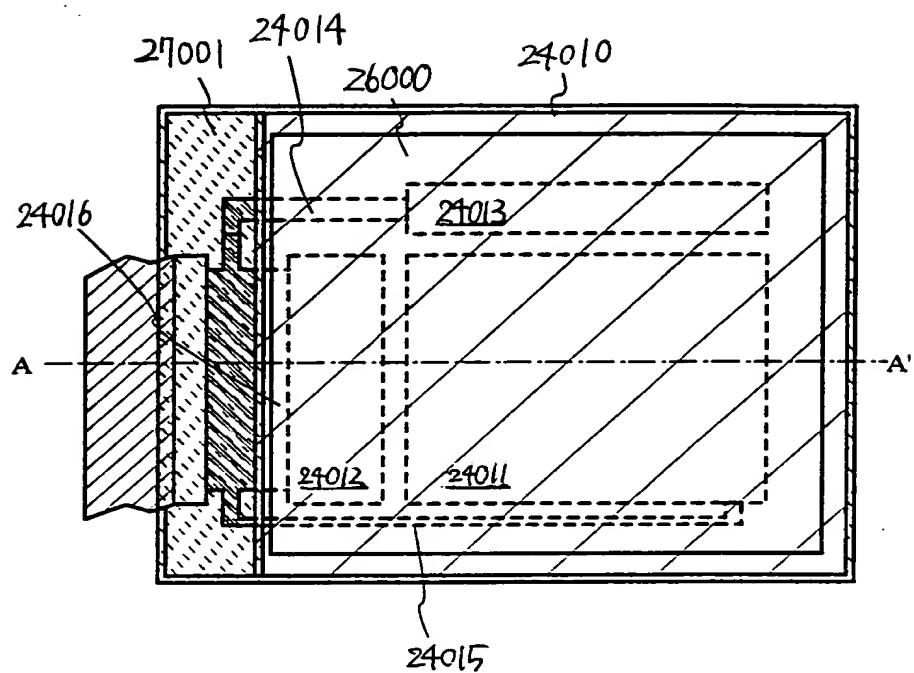


FIG. 32A

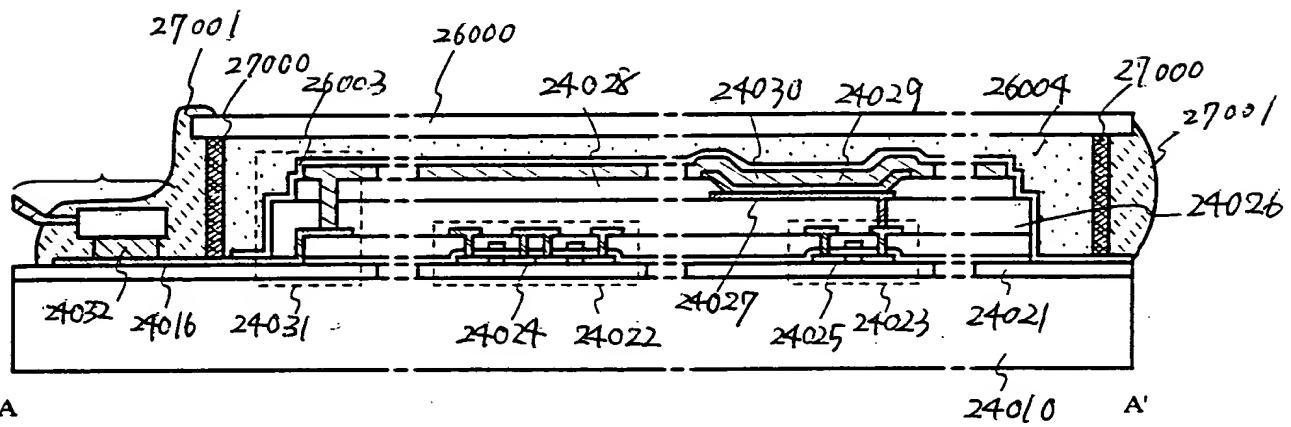


FIG. 32B

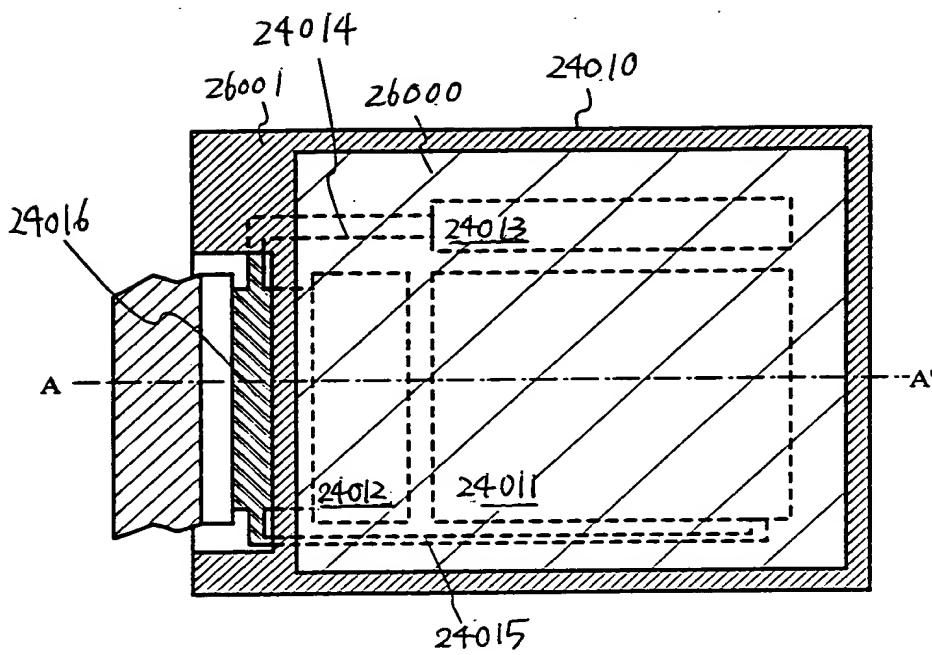


FIG. 33A

00547825 022600

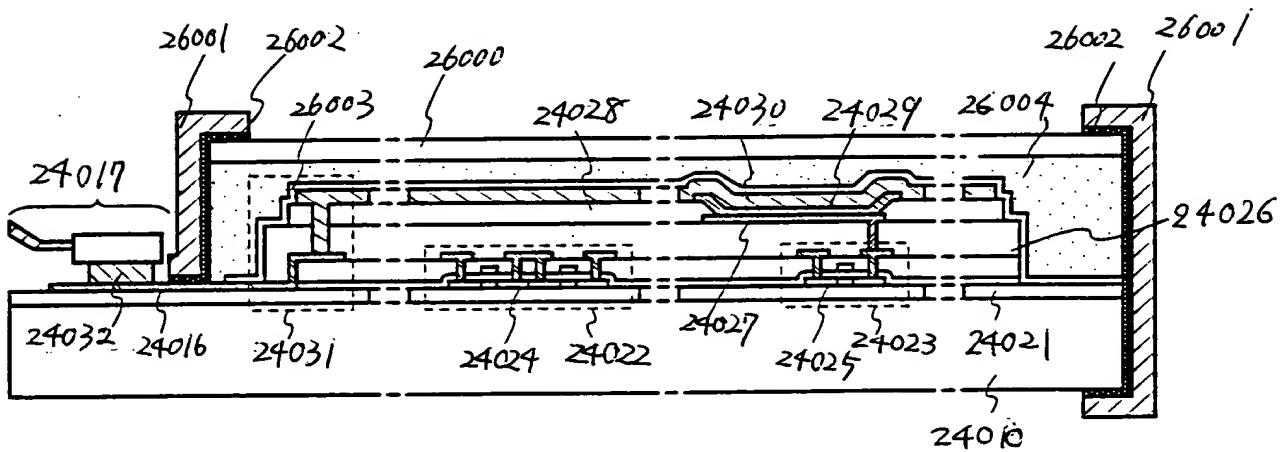
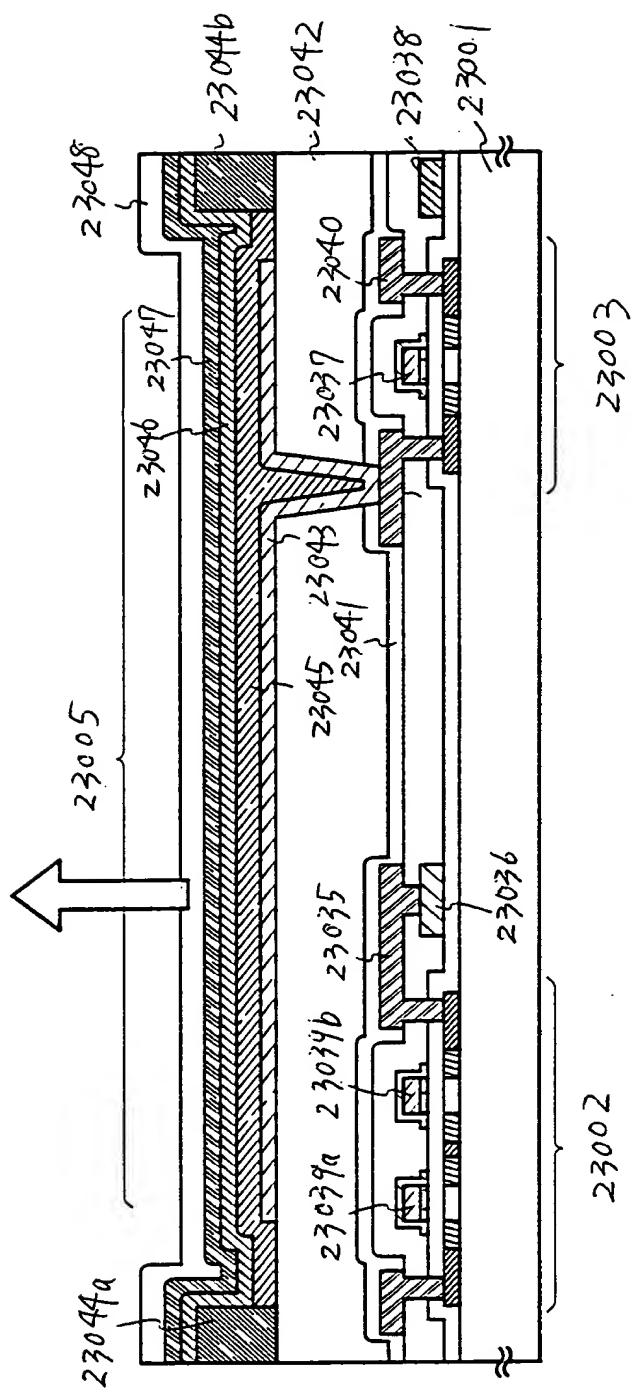


FIG. 33B

F | G. 34



000000000000000000000000

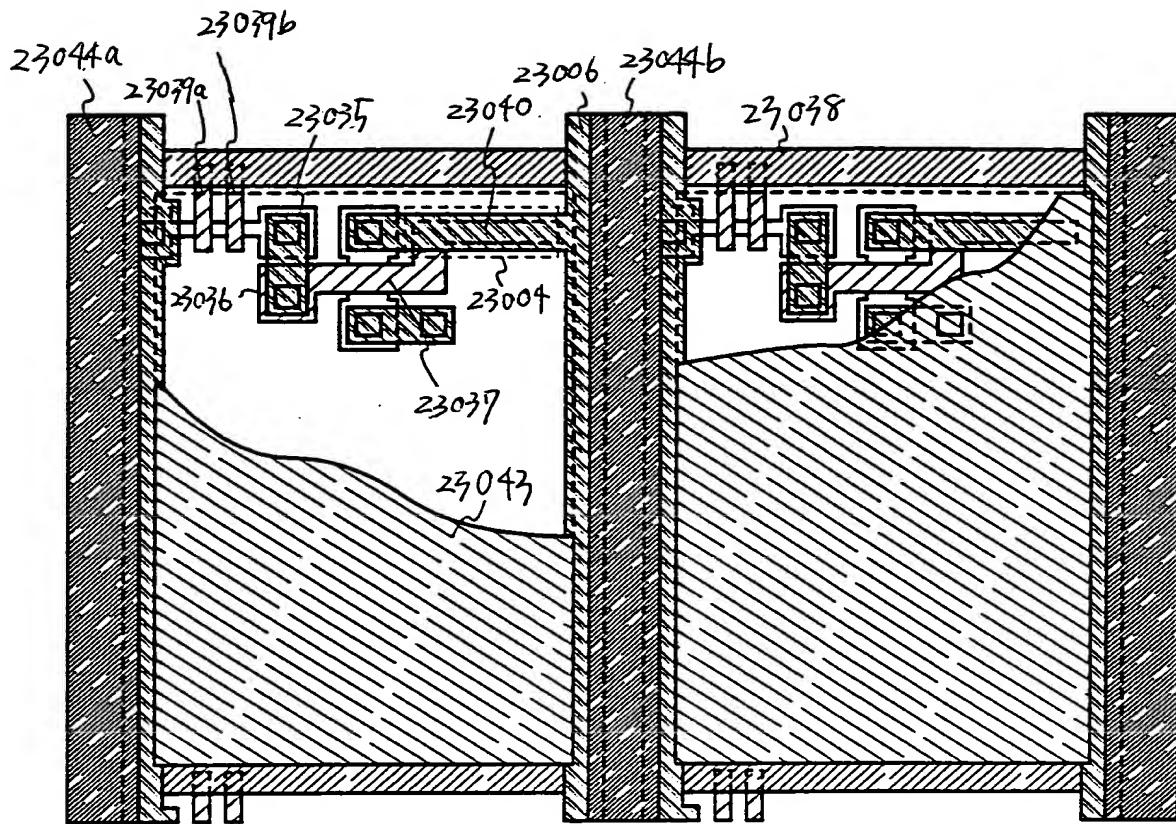


FIG. 35A

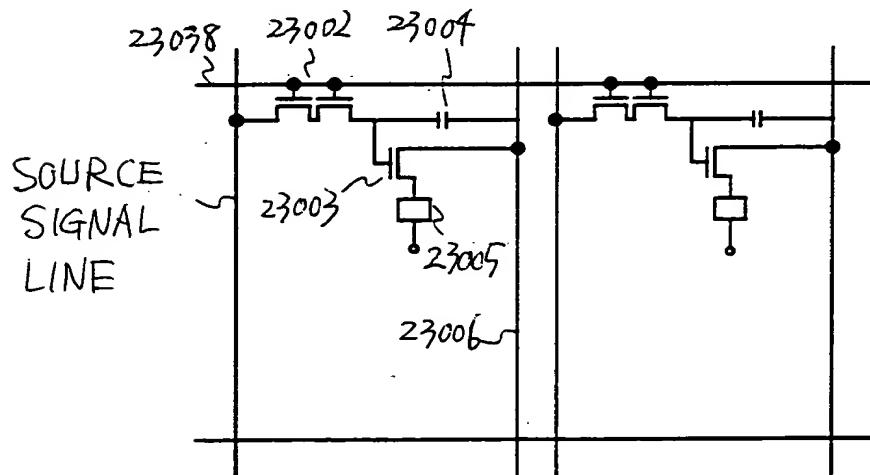
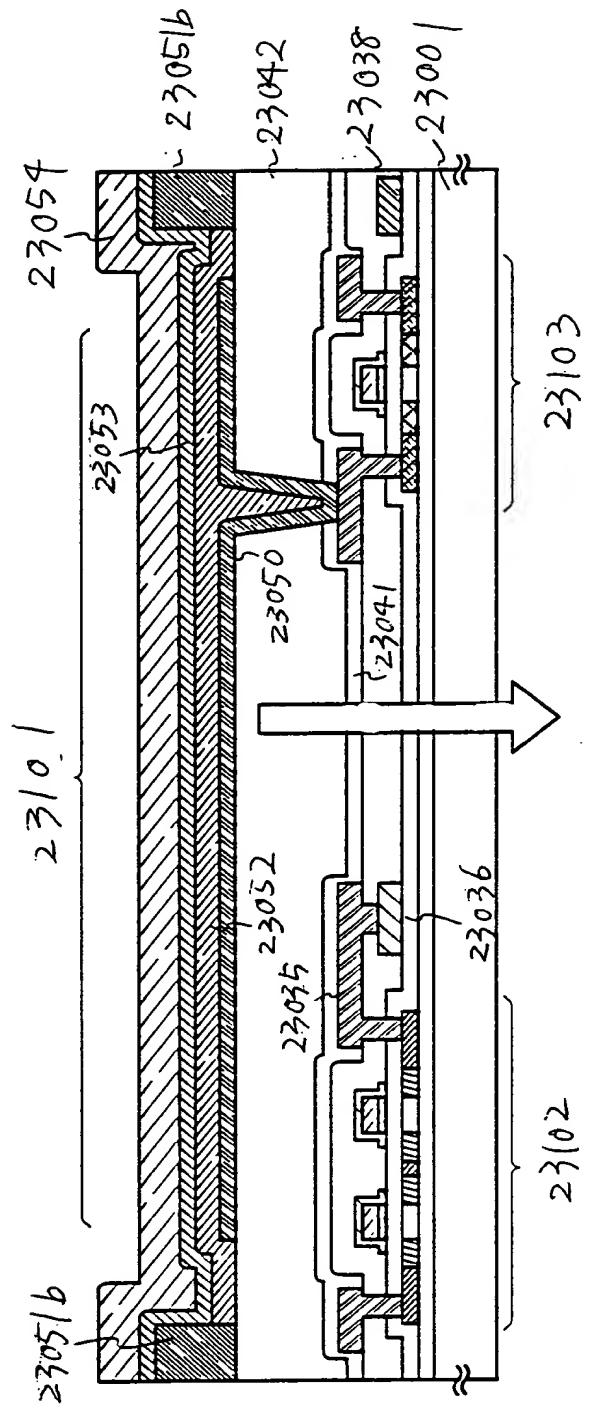


FIG. 35B

002230 \* 52020500



F16.36

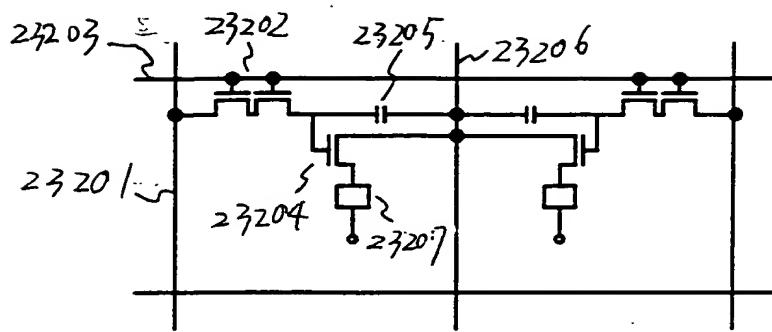


FIG. 37A

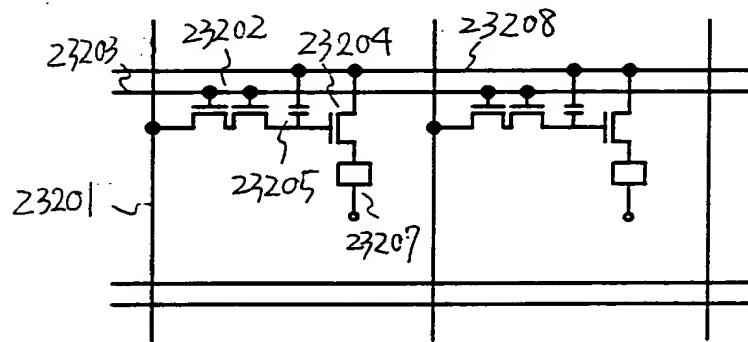


FIG. 37B

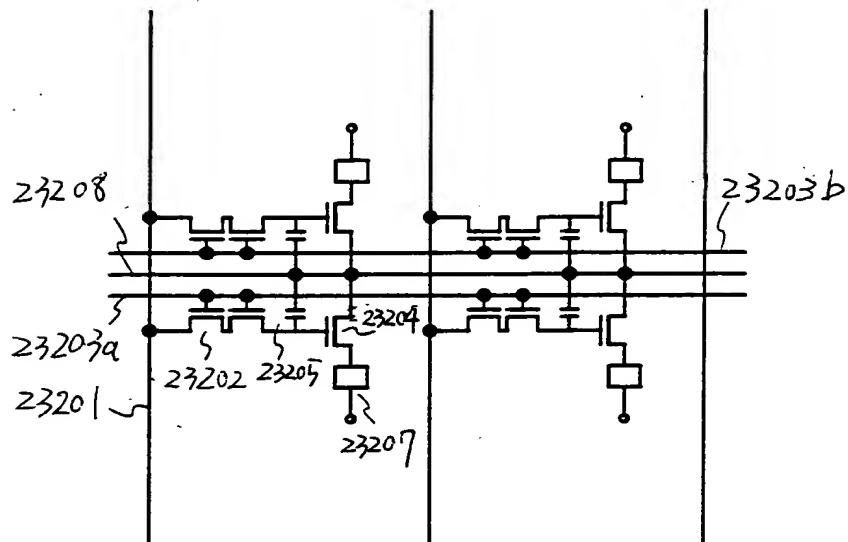


FIG. 37C